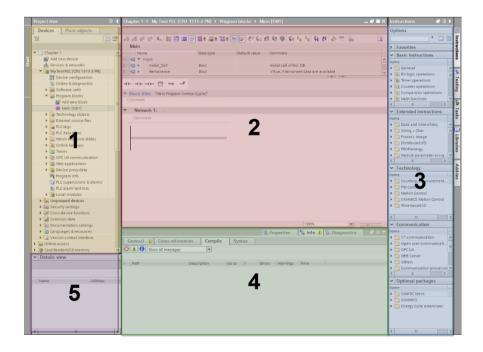
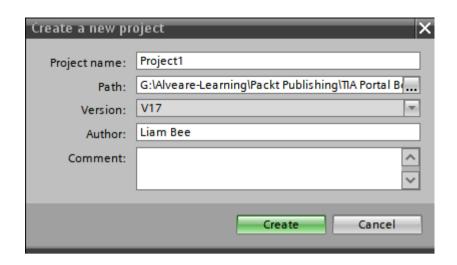
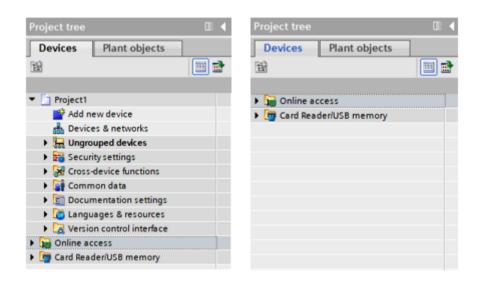
Chapter 1: Starting a New Project with TIA Portal



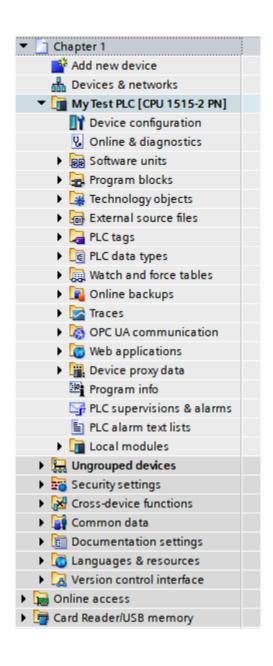


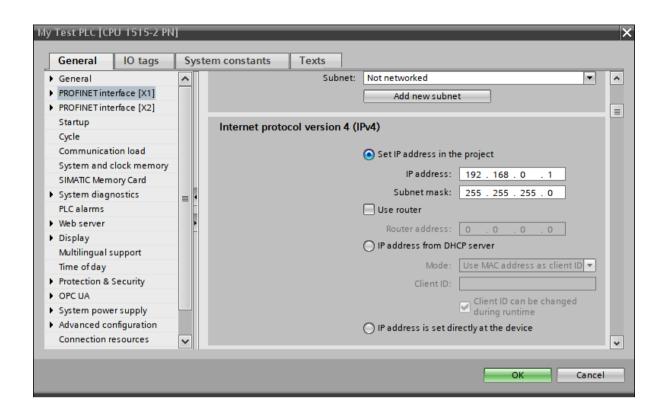


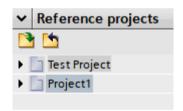




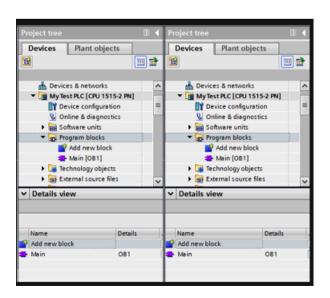


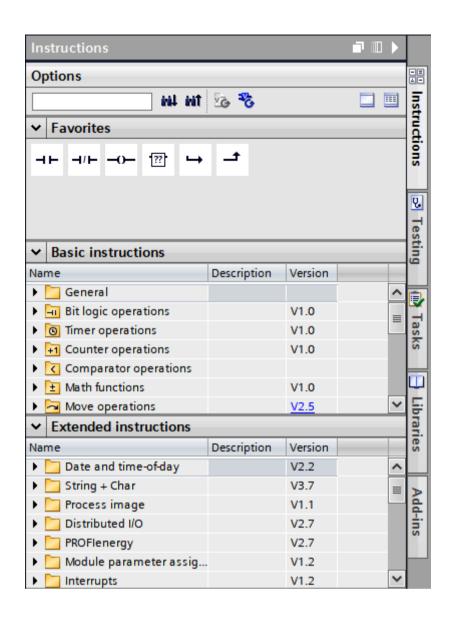


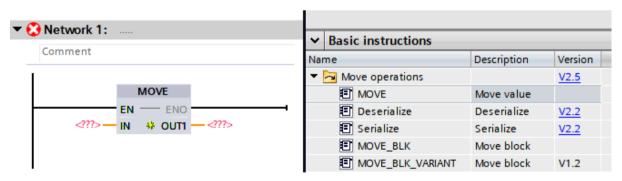


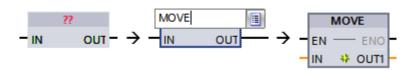




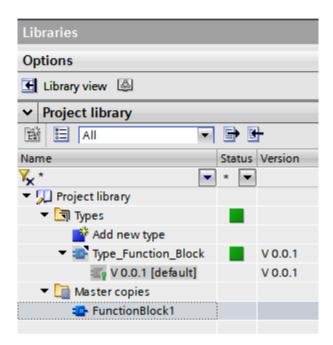


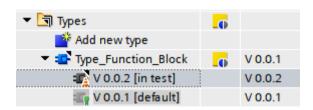


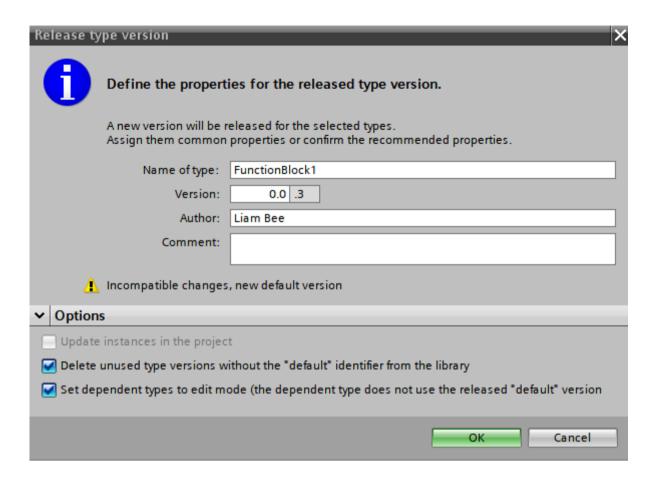


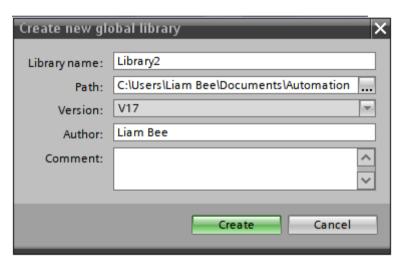


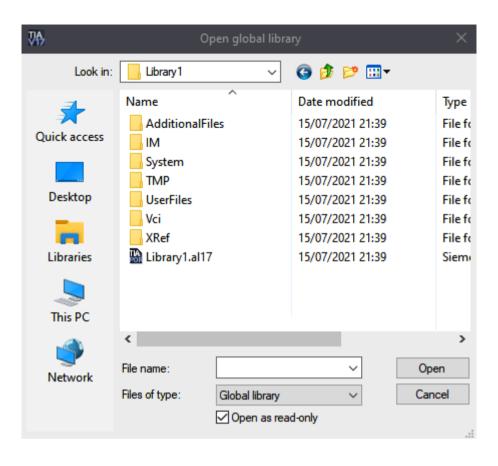


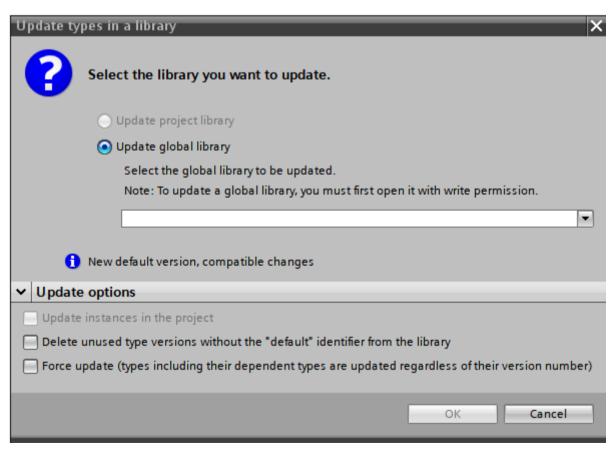




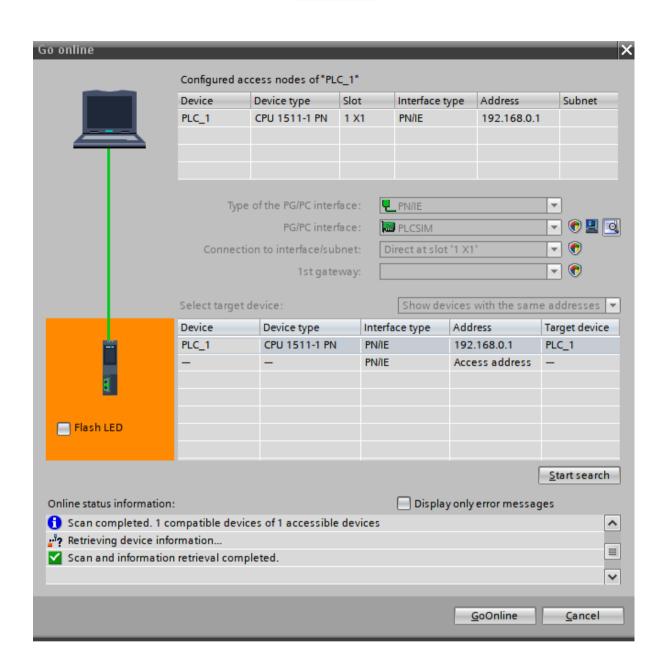


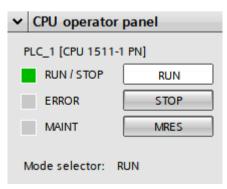










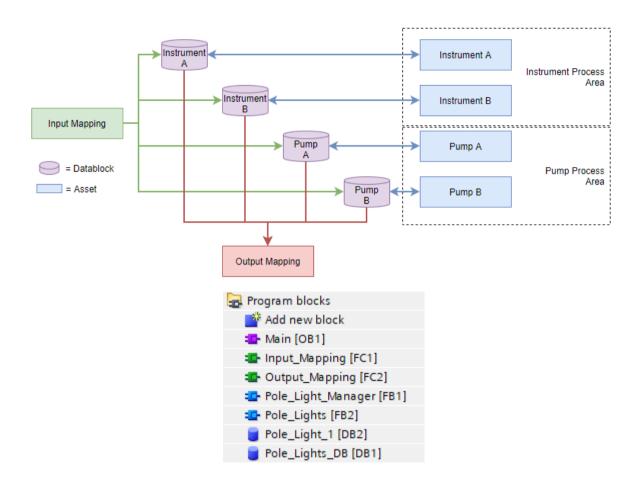


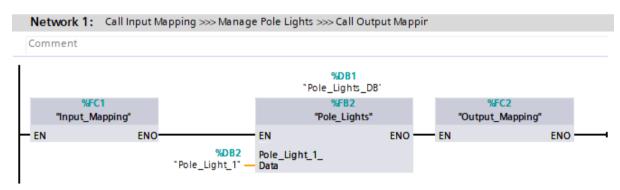


✓ Call hierarchy

Main [OB1] - NW 1

Chapter 2: Creating Objects and How These Fit Together



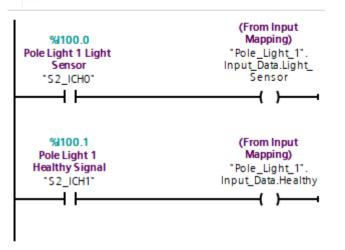


▼ Block title: Input Mapping

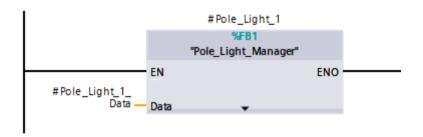
Comment

▼ Network 1: Pole Light 1

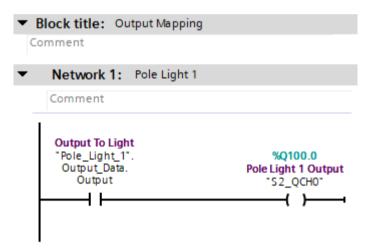
Comment

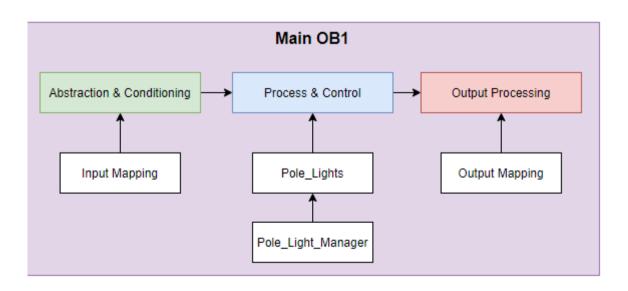




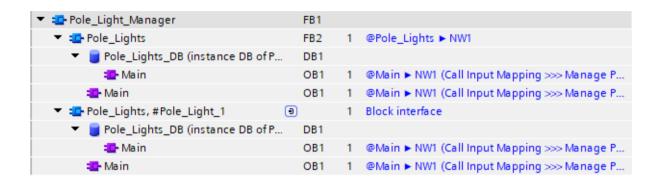


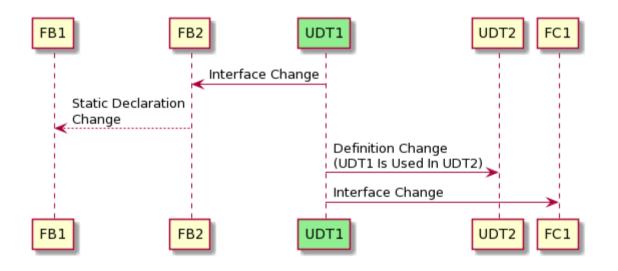
	Pole_Light_1										
		Nai	me	Data type							
1	€00	•	Sta	atic							
2	€00	•	•	Input_Data	Struct						
3	€00		•	Light_Sensor	Bool						
4	€00		•	Healthy	Bool						
5	€00	•	•	Control_Data	Struct						
6	€00		•	Light_Sensor_Active	Bool						
7	€00	•	•	Status_Data	Struct						
8	€11		•	Light_Flashes	Lint						
9	€00		•	Healthy	Bool						
10	€00		•	Maintenance_Req	Bool						
11	€00	٠	•	SCADA_Data	Struct						
12	€00		•	▶ From_SCADA	Struct						
13	€00		•	► To_SCADA	Struct						
14	€00	٠	•	Output_Data	Struct						
15	€		•	Output	Bool						

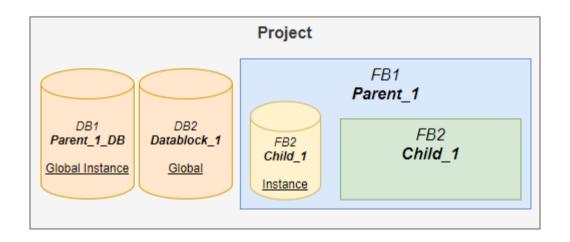


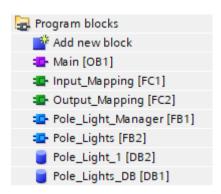


Call structure of PLC_1										
Call structure	- !	Address	Call freq	Details						
▼		OB1								
► ■ Input_Mapping		FC1	1	@Main ► NW1 (Call Input Mapping >>> Manage P						
Output_Mapping		FC2	1	@Main ► NW1 (Call Input Mapping >>> Manage P						
Pole_Light_1 (Data block derived from UDT_Pole_Light)		DB2	1	@Main ► NW1 (Call Input Mapping >>> Manage P						
Pole_Lights, Pole_Lights_DB		FB2, DB1	1	@Main ► NW1 (Call Input Mapping >>> Manage P						
Pole_Light_Manager, #Pole_Light_1	\equiv	FB1	1	@Pole_Lights ► NW1						
Pole_Light_Manager, #Pole_Light_1	•	FB1	1	Block interface						

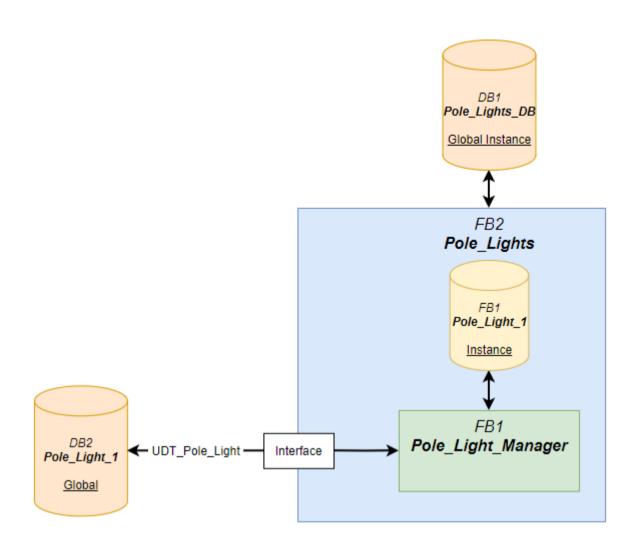






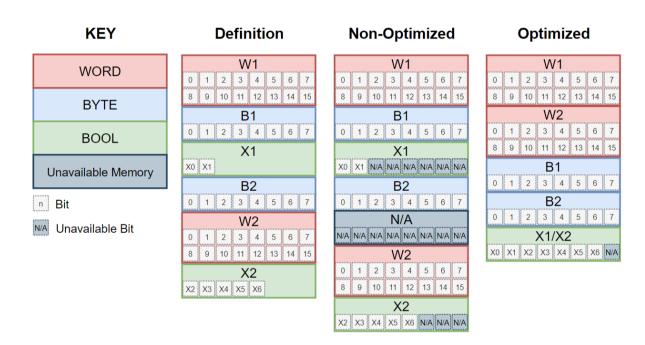


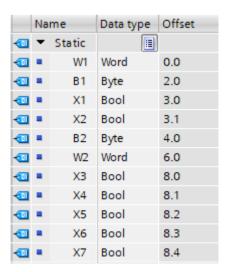
	Pole_Lights_DB											
		Name	Data type									
1	1	Input	=									
2	1	Output										
3	1	▼ InOut										
4	1	Pole_Light_1_Data	"UDT_Pole_Light"									
5	€00	▼ Static										
6	€	■ ▼ Pole_Light_1	"Pole_Light_Manager"									
7	1	Input										
8	€11	■ ▼ Output										
9	1	Ref_Light_Active	Bool									
10	1	Ref_Light_Healthy	Bool									
11	1	■ ▼ InOut										
12	1	Data	"UDT_Pole_Light"									
13	1	■ ▼ Static										
14	1	Light_Sensor_Delay	TON_TIME									
15	€	On_Duration	TOF_TIME									
16	1	Off_Duration	TON_TIME									
17	1	On_Rising_Edge	Bool									



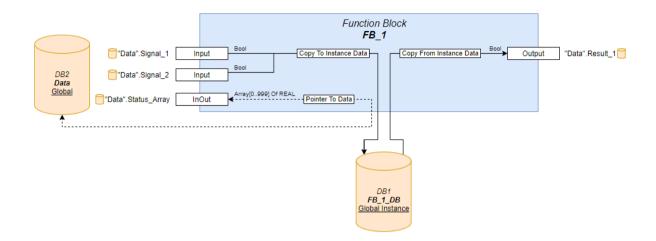
	Pole_Light_1												
		Na	Data type										
1	1	•	St	atic									
2	1	•	٠	Input_Data	Struct								
3	1	•	٠	Control_Data	Struct								
4	1	٠	٠	Status_Data	Struct								
5	1	•	٠	SCADA_Data	Struct								
6	1	•	٠	Output_Data	Struct								

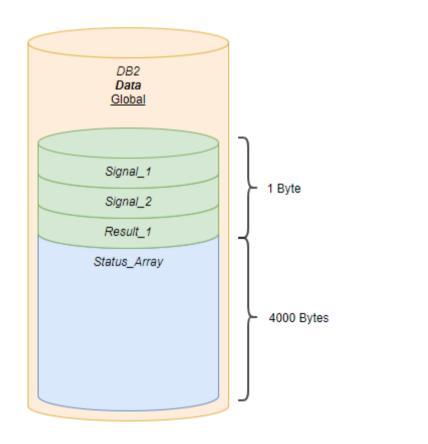
	Pole_Lights_DB											
		Na	me		Data type							
1	€11		Inpu	ut								
2	4 11		Out	put								
3	1	•	InO	ut								
4	1	•	F	Pole_Light_1_Data	"UDT_Pole_Light"							
5	1	•	Stat	tic								
6	1	•	•	Pole_Light_1	"Pole_Light_Manager"							
7	1		•	Input								
8	1		• 1	Output								
9	1		• 1	▶ InOut								
10	1		• 1	▶ Static								

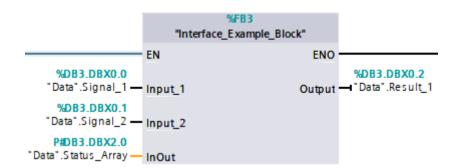










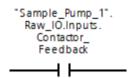


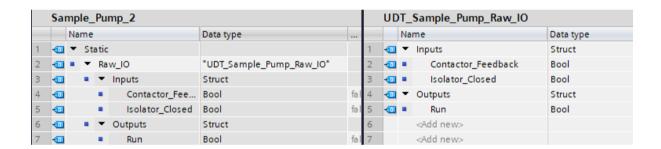
4	1	•	Output		
5	1	٠	Output	Array[0999] of Real	2.0
6	1	•	Outputs_1	Array[0999] of Real	4002.0
7	1	•	Outputs_2	Array[0999] of Real	8002.0
8		•	<add new=""></add>		
9	1	•	InOut		
10	1	•	▶ InOut	Array[0999] of Real	12002.0
11	1	٠	▶ InOut_1	Array[0999] of Real	12008.0
12	€	•	▶ InOut_2	Array[0999] of Real	12014.0

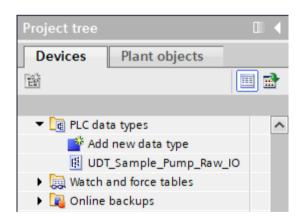
InC	Out		
•	InOut	Array[0999] of Real	12002.0
•	InOut[0]	Real	0.0
•	InOut[1]	Real	4.0
•	InOut[2]	Real	8.0
•	InOut[3]	Real	12.0
•	InOut[4]	Real	16.0
•	InOut[5]	Real	20.0

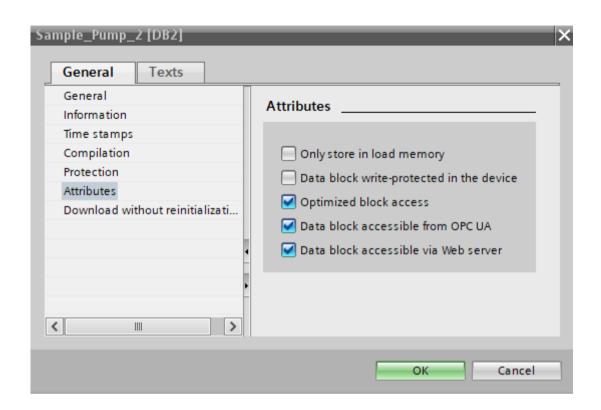
Chapter 3: Structures: Structs and User-Defined Types

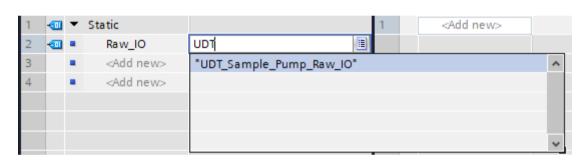
	Sample_Pump_2							Sample_Pump_1					
		Na	me			Data type			Na	me			Data type
1	1	■ ▼ Static					1	1	▼ Static				
2	1	•	•	Ra	w_IO	Struct	2	1	•	•	Ra	w_IO	Struct
3	1		•	•	Inputs	Struct	3	1			•	Inputs	Struct
4	1			•	Contactor_Fee	Bool	4	1			•	Contactor_Fee	Bool
5	1			•	Isolator_Closed	Bool	5	1			•	Isolator_Closed	Bool
6	1		•	•	Outputs	Struct	6	1			•	Outputs	Struct
7	1				Run	Bool	7	1			•	Run	Bool

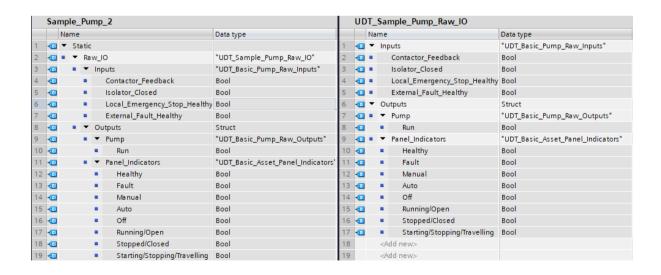


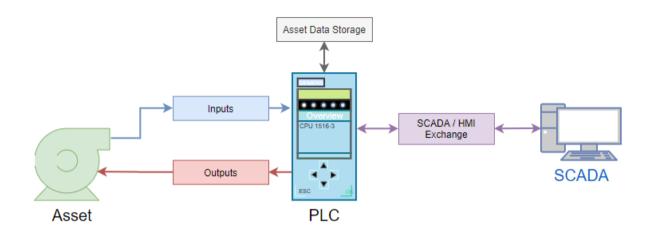




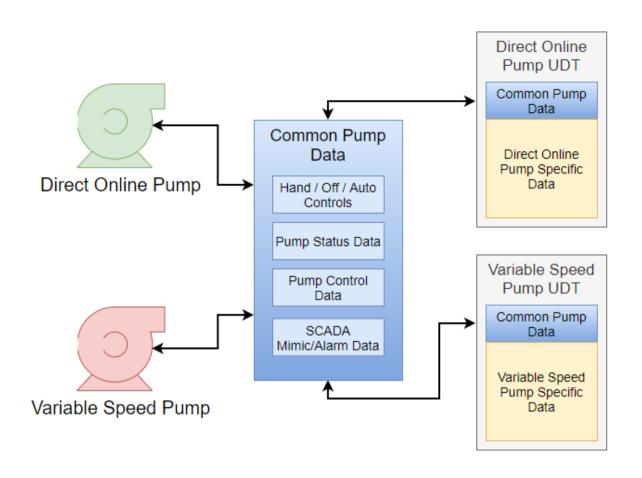


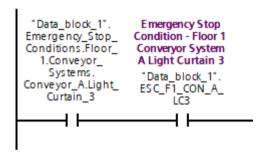


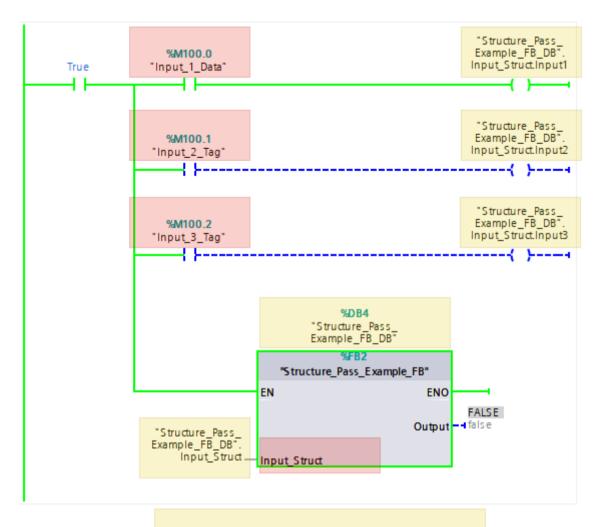




	UDT_Direct_Online_Pump									
		Nam	e	Data type						
1	1	•	O_Layer	Struct						
2	€	. 1	Pump_Inputs	"UDT_Basic_Pump_Raw_Inputs"						
3	€	. 1	Pump_Outputs	"UDT_Basic_Pump_Raw_Outputs"						
4	1	.	Panel_Outputs	"UDT_Basic_Asset_Panel_Indicators"						
5	1	v /	Asset_Data	Struct						
6	1		Hours_Run	Real						
7	1		Number_Of_Starts	DInt						
8	1		Number_Of_Failures	Dint						
9	1	▼ 5	CADA_Data	"UDT_Basic_Pump_SCADA_Data"						
10	€	• •	Read	Struct						
11	€11		▶ HOA	Struct						
12	1		Asset_Status	Struct						
13	1		▶ Control_Status	Struct						
14	1		▶ SCADA_Control	Struct						
15	1	• •	Write	Struct						
16	1		▶ HOA	Struct						
17	1		▶ Control_Commands	Struct						

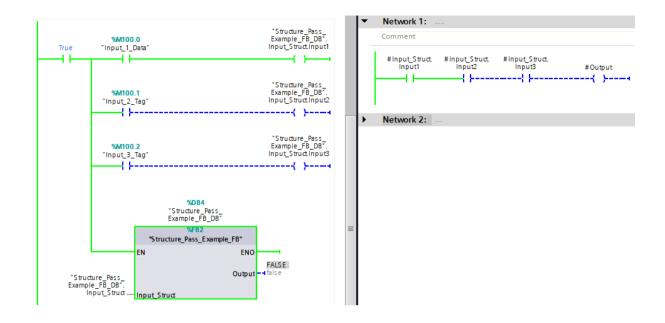






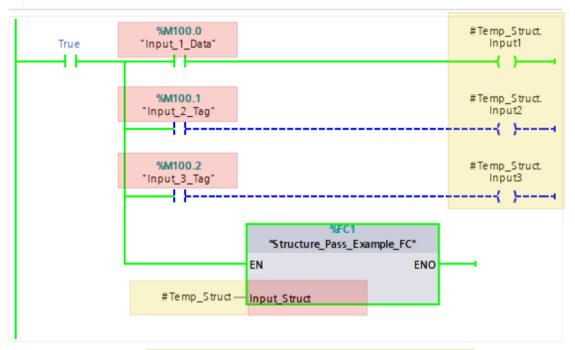
Instance data accessed directly

Interface data passed to instance data



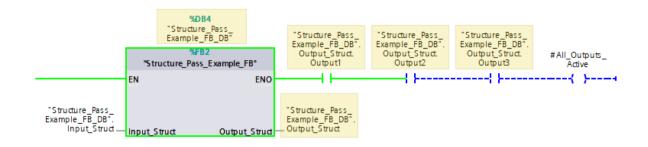


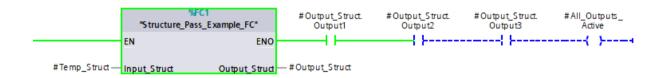
Comment

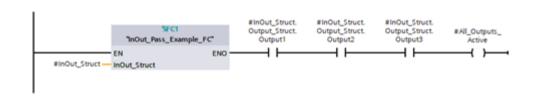


Temporary structure data

Interface data passed to temporary structure data



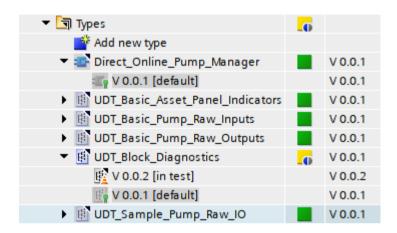


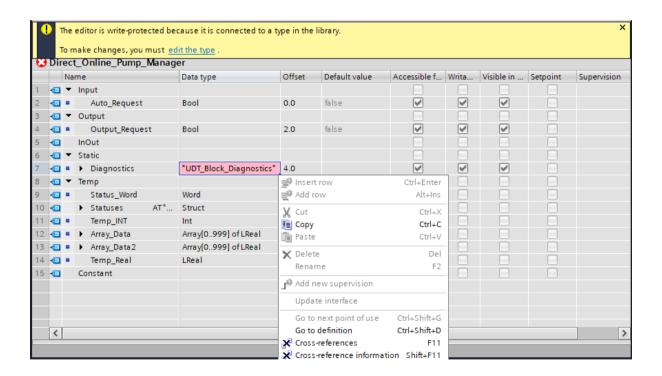


6	4	•	St	atic			
7	1	•	•	Diagnostics	"UDT_Block_Diagnostics"		
8	1		•	Last_Called	LDT	LDT#1970-01-01-	LDT#2021-08-05-17:56:27.167442490
9	1		•	Call_Count	Dint	0	330347
10	1		•	Runtime	LReal	0.0	1.35910541332928E-05
11	1		•	Runtime_Memory	LReal	0.0	16#0000_014A_8810_FB16

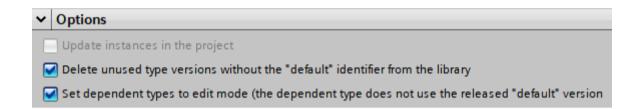
10	1	▼ Te	mp			
11	1		Status_Word		Word	0.0
12	1	•	Statuses	AT "Status_Word"	Struct	0.0
13	1		Healthy		Bool	0.0
14	1		Ready_To_Rur	1	Bool	0.1
15	1		Request		Bool	0.2
16	1		Torque_OK		Bool	0.3
17	€11		Temp_OK		Bool	0.4
18	€11		Voltage_OK		Bool	0.5
19	€11		Break_Off		Bool	0.6
20	€		Forward_Dire	ction	Bool	0.7
21	1		Reverse_Direc	ction	Bool	1.0

▼ 🔄 Types	
💣 Add new type	
 Direct_Online_Pump_Manager 	V 0.0.1
▶ ■ UDT_Basic_Asset_Panel_Indicators	V 0.0.1
▶ I UDT_Basic_Pump_Raw_Inputs	V 0.0.1
▶ I UDT_Basic_Pump_Raw_Outputs	V 0.0.1
▶ I UDT_Block_Diagnostics	V 0.0.1
▶ 圆 UDT_Sample_Pump_Raw_IO	V 0.0.1





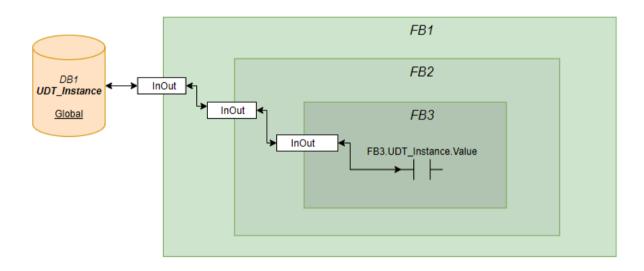
Co	mpiling finished (errors: 1; warnings: 0)		
1	Path	Description	Go to
3	▼ PLC_1		7
0	▼ PLC data types		7
0	UDT_Block_Diagnostics (UDT)	The data type was successfully updated.	7
3	▼ Program blocks		7
3	▼ Direct_Online_Pump_Manager (FB1)		7
3	Interface	The interface of the block or data type contains incompatible changes	7
0	Main (OB1)	Block was successfully compiled.	7
3	Direct_Online_Pump_Manager_DB (DB3)	Block was successfully compiled.	7
3		Compiling finished (errors: 1; warnings: 0)	

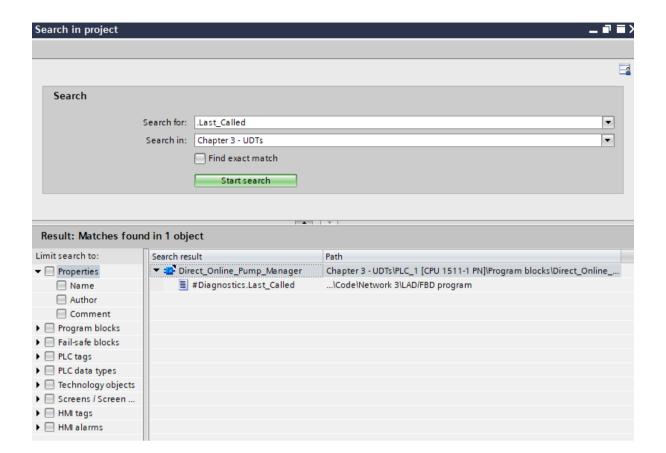


▼ 🞵 Project library		
▼ 🔄 Types		
💣 Add new type		
▼		V 0.0.1
■ V 0.0.2 [in test]		V 0.0.2
👣 V 0.0.1 [default]		V 0.0.1
▶ I UDT_Basic_Asset_Panel_Indicators		V 0.0.1
UDT_Basic_Pump_Raw_Inputs		V 0.0.1
▶ I UDT_Basic_Pump_Raw_Outputs		V 0.0.1
▼ I UDT_Block_Diagnostics		V 0.0.2
		V 0.0.2
₹ V 0.0.1		V 0.0.1
▶ I UDT_Sample_Pump_Raw_IO		V 0.0.1

▼ 🎵 Project library		
▼ 🔄 Types		
💣 Add new type		
▼ irect_Online_Pump_Manager		V 0.0.2
		V 0.0.2
 UDT_Basic_Asset_Panel_Indicators 		V 0.0.1
 UDT_Basic_Pump_Raw_Inputs 		V 0.0.1
▶ ⑤ UDT_Basic_Pump_Raw_Outputs		V 0.0.1
▼ I UDT_Block_Diagnostics		V 0.0.2
√ V 0.0.2 [default]		V 0.0.2
▶ I UDT_Sample_Pump_Raw_IO		V 0.0.1



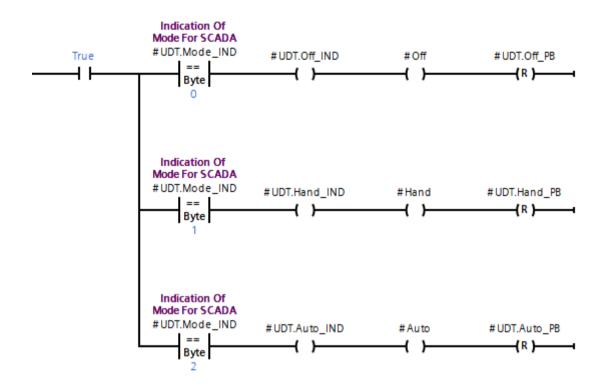


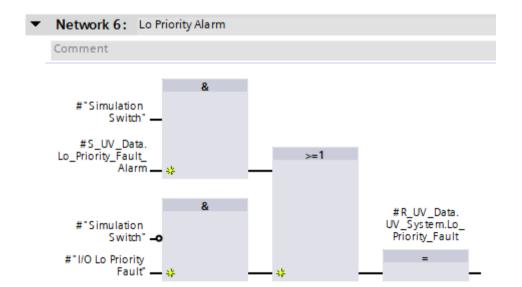


Chapter 4: PLC Programming and Languages

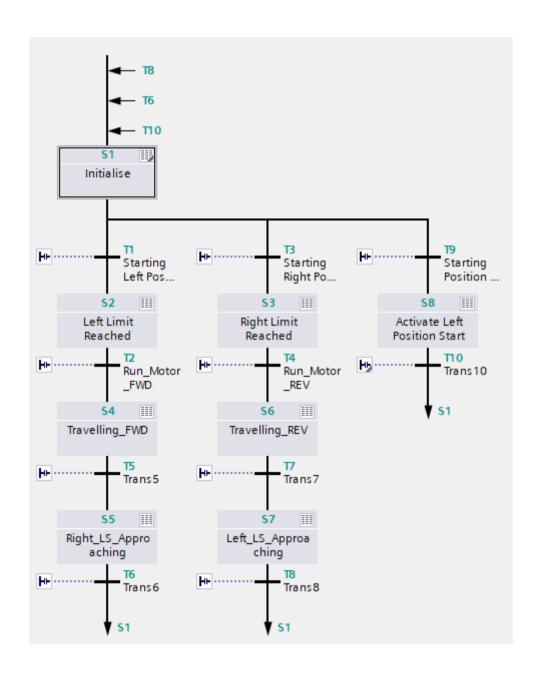
Network 5: Set Boolean Outputs to be used outside of the block

Comment





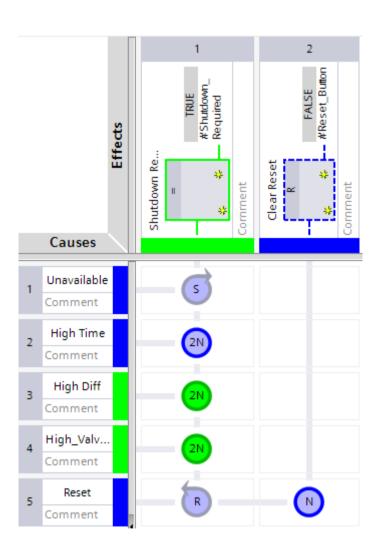
END_IF;

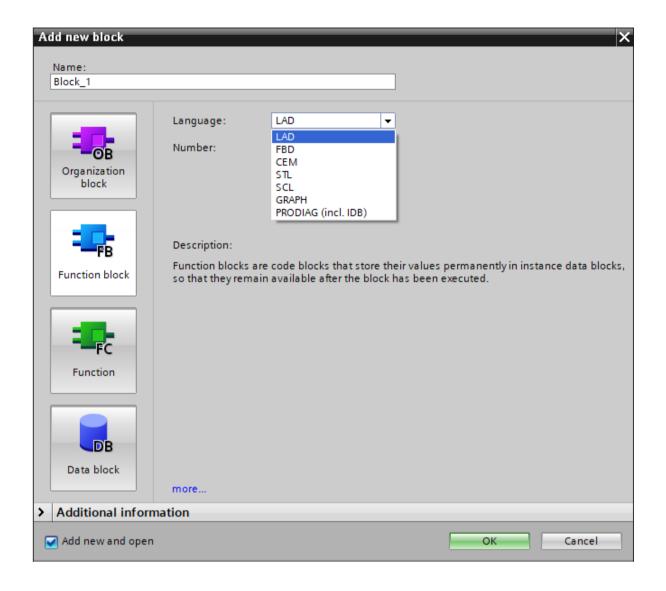


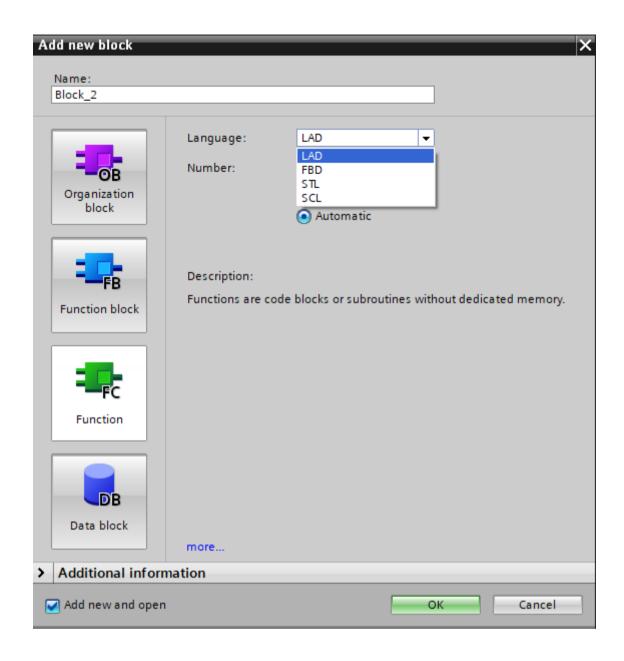
Network 1:

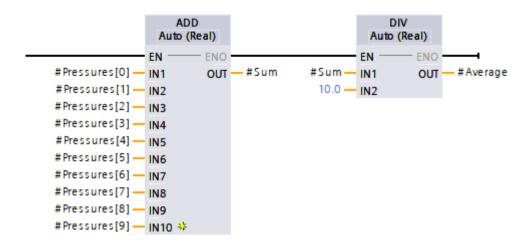
Comment

```
//Amplify signal
2
          L
                #Signal
 3
                20
          L
                                                           20
 4
          ×Ι
 5
          Т
                "Amplified_Signal"
                                                          %MW10
 6
7
   //Check Enable Signals And Enable Output Signal
8
                #Enable_1
9
          Α(
                #Enable 2
10
          0
                #Enable_3
11
          ON
12
13
                #Enable_Signal
14
```







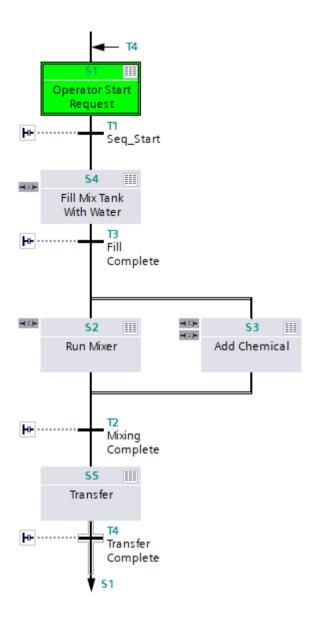


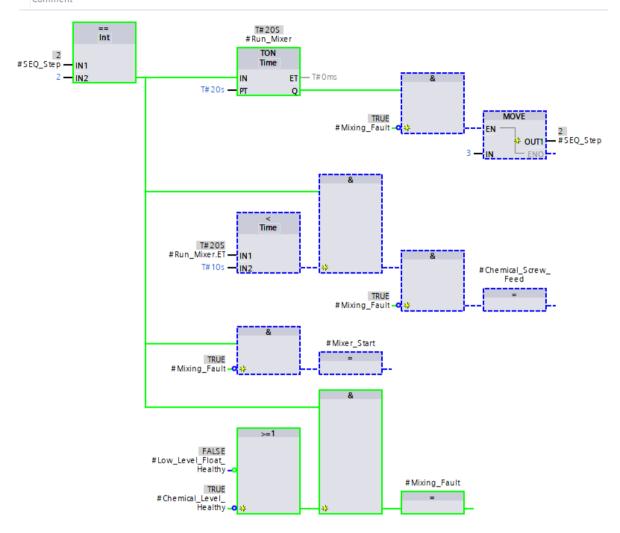
```
FOR #i := 0 TO 9 BY 1 DO

#Sum += #Pressures[#i];

END_FOR;

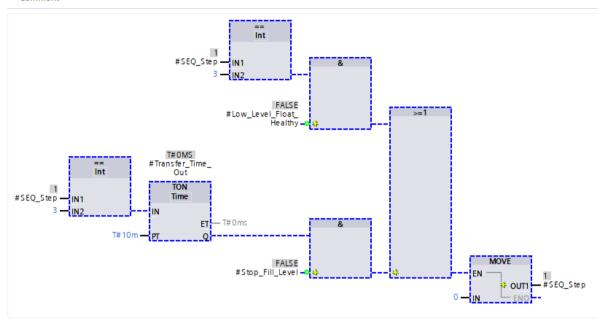
#Average := #Sum / 10;
```





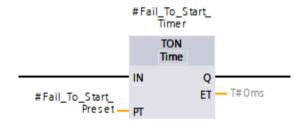
▼ Network 4: Transfer

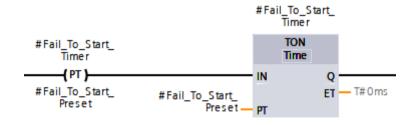
Comment

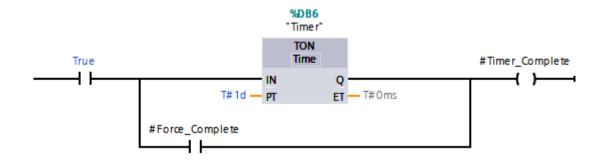


▼ Network 5: Fill Fault

<pre>1 #Clock_Trigger(CLK:="Clock_1Hz");</pre>		"Clock_1Hz"	TRUE
2 3 PIF #SEQ_Step = 1 AND #Clock_Trigger.Q THEN	F	Result	FALSE
4 #FillCount += 1;		#FillCount	71 → 72
5 #Fill_Fault := #FillCount > 1200;)	#Fill_Fault	FALSE
6 END_IF;			







```
"Time r"

True Time #Timer_Complete

IN Q
T#1d PT ET T#0ms

True #Force_Complete
```

```
▼ Interlock -(c)-: ....
```

```
#Chemical_Level_
Healthy Interlock

(C)
```

▼ Supervision -(v)-:

```
#Chemical_Level_
Healthy Supervision

(V)
```

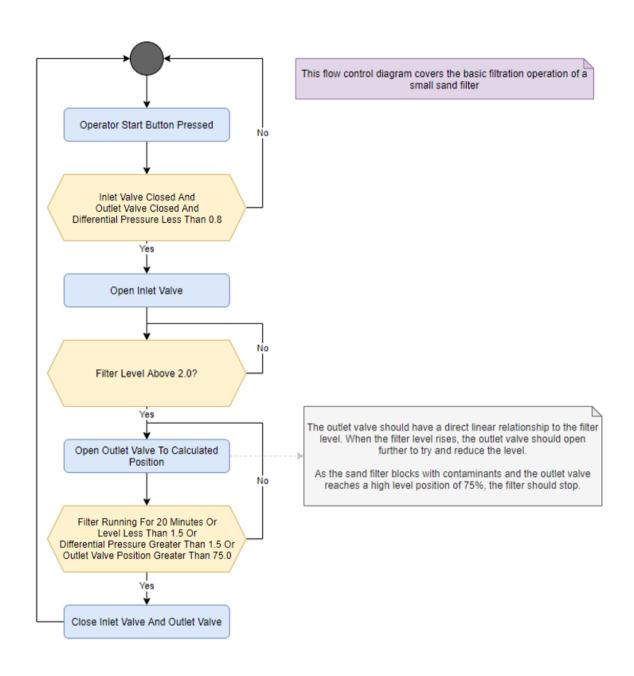
▼ Actions: ...

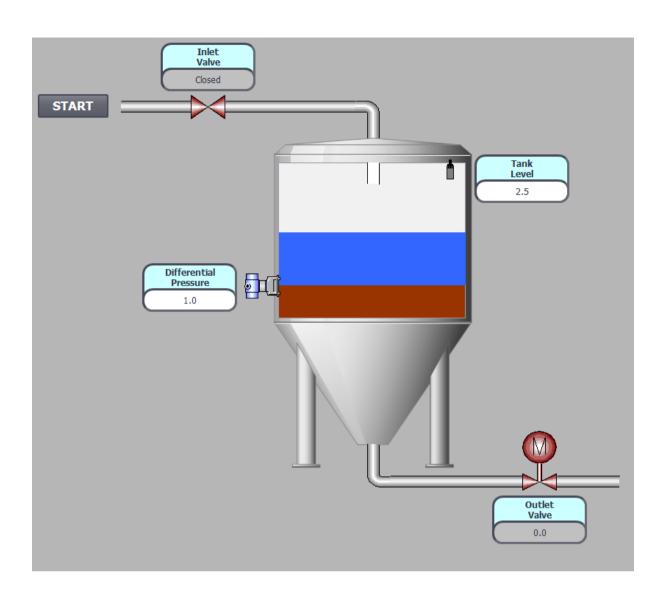
-(c)-	Interlock	Event	Qualifier	Action	
-(∨)-			L -Set for limited time <add new=""></add>	#Chemical_Screw_Feed,T#10s	
	#Chemical_Screw_Feed		ed		

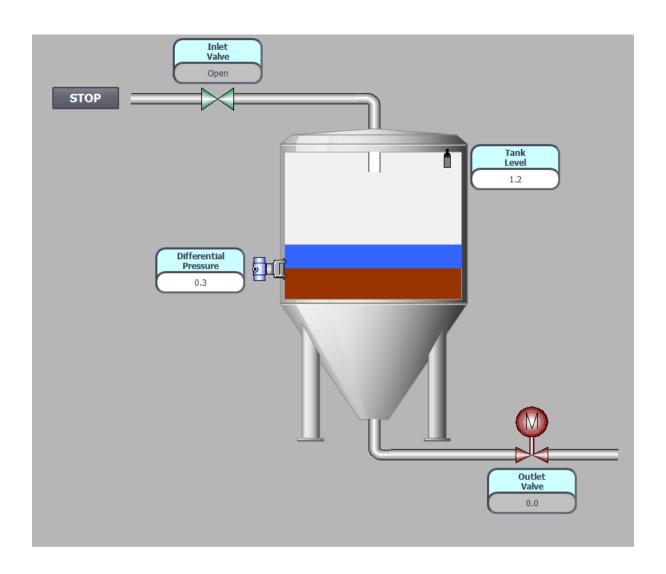
#Filter_Alarm_Active := #Alarm_Word.13;

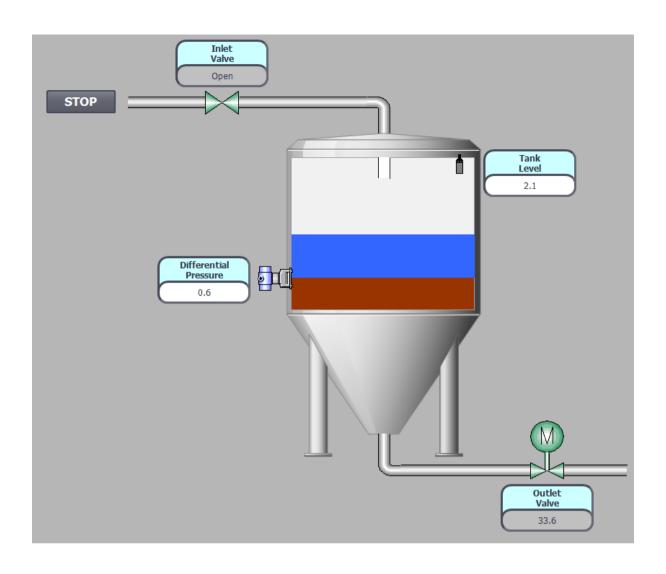
#Filter_Alarm_Active := #Alarm_Word.%X13;

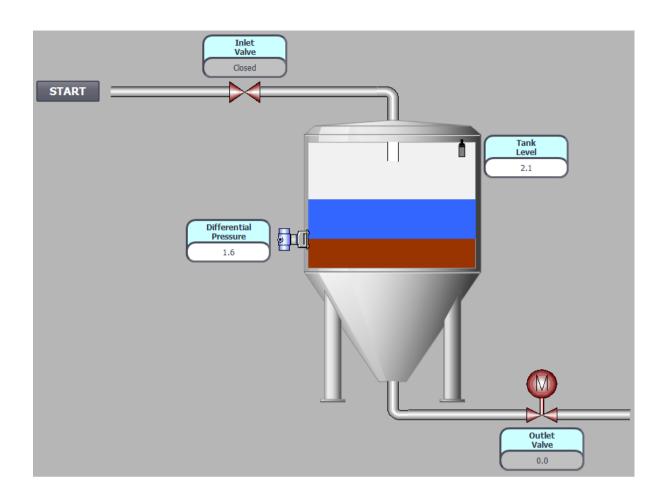
Chapter 5: Working with Languages in TIA Portal

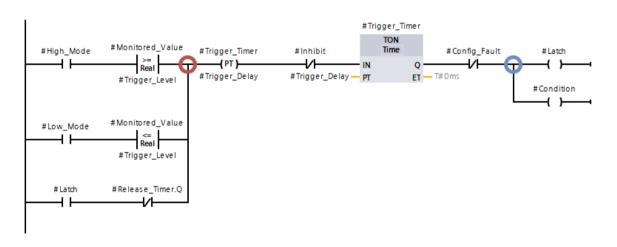




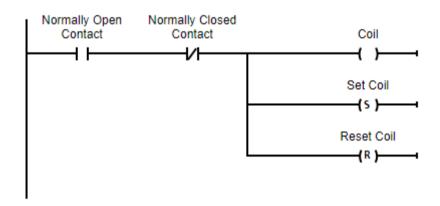








∨ Basic instructions					
Name	Description	Version			
▶ 🛅 General					
▶ 📶 Bit logic operations		V1.0			
▶ S Timer operations		V1.0			
▶ 🚹 Counter operations		V1.0			
▶ Comparator operations					
▶ ± Math functions		V1.0			
▶ Move operations		<u>V2.5</u>			
 Conversion operations 					
▶ 🔐 Program control operati		V1.1			
▶ ➡ Word logic operations		V1.4			
Shift and rotate					
▶ ETC Legacy		V2.6			

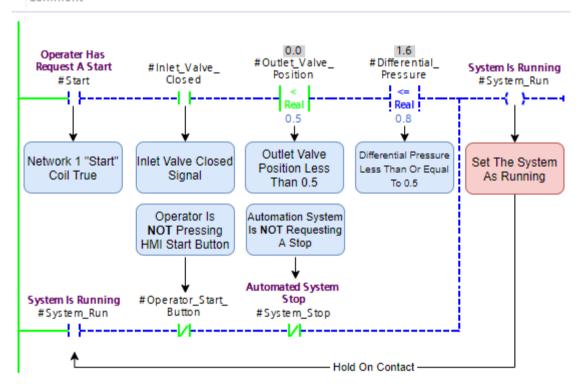


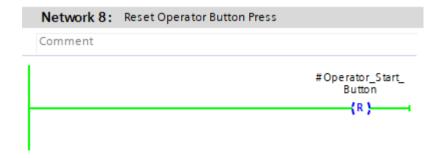




Network 1: Operator Start Button Comment Operater Has Request A Start #Operator_Start_ System Is Running Button #System_Run #Start --{ P }----#Start_Memory FALSE Operator pressed Pulse the The system is the START Start variable as **NOT** running Button on HMI TRUE for 1 scan

▼ Network 2: Requirements For Start



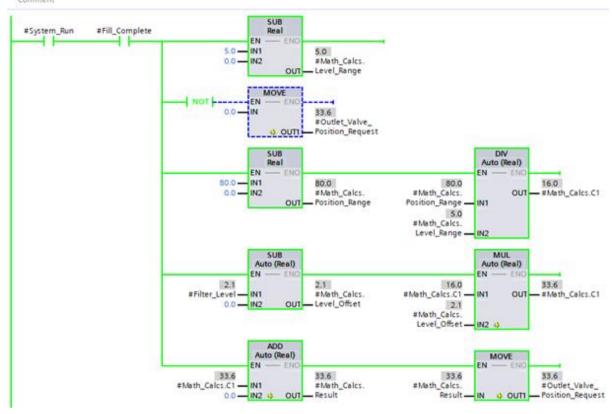


Network 3: System Running - Open Inlet Valve Comment System Is Running #Inlet_Valve_ #System_Run Open_Request

Network 4: System Running - Manage Fill

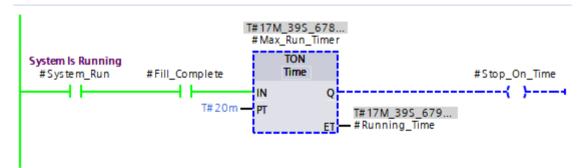
```
2.1
#Filter_Level
System Is Running
#System_Run
                                                                       #Fill_Complete
                           >
Real
       4 F
                                                                           \prec \succ
                            2.0
                      #Fill_Complete
                           \dashv \vdash
                                            2.1
#Filter_Level
                      #Fill_Complete
                                                                       #Level_Fault
                                                <
                                                            ----- }-----
                                                Real
                                                 1.5
```

Network 5: System Running - Calculate Required Output Valve Position

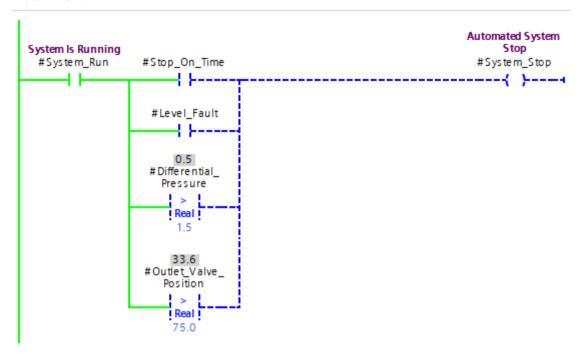


▼ Network 6: System Running - Calculate Time Running

Comment



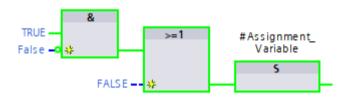
▼ Network 7: System Running - Automatic Stop

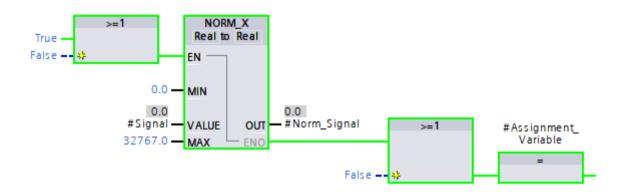


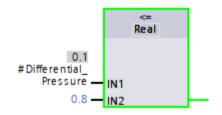
∨ Basic instructions			
Name	Description	Version	
▶ 🗀 General			
▶ 📶 Bit logic operations		V1.0	
▶		V1.0	
▶ 🚹 Counter operations		V1.0	
 Comparator operations 			
▶ ± Math functions		V1.0	
 Move operations 		<u>V2.5</u>	
 Conversion operations 			
▶ 🙌 Program control operations		V1.1	
 Word logic operations 		V1.4	
▶ 😝 Shift and rotate			
▶ ETC Legacy		V2.6	

▼ 🔄 Bit logic operations	
■ &	AND logic operation [F9]
■ >=1	OR logic operation [F10]
 x	EXCLUSIVE OR logic operation

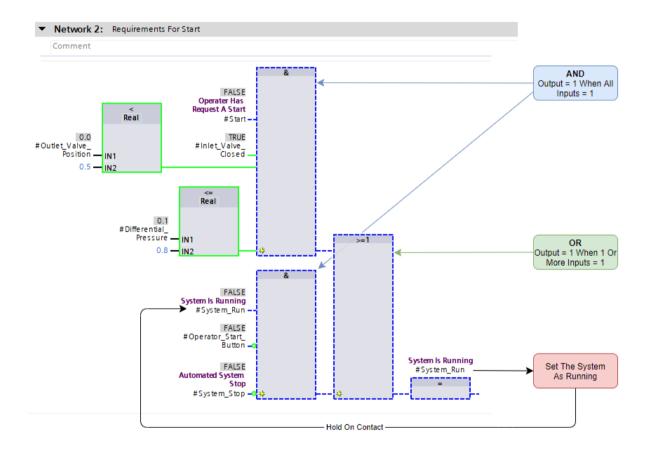


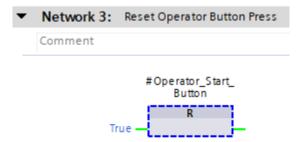






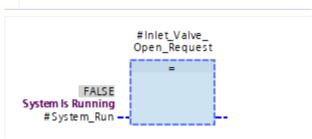
Network 1: Operator Start Button Comment #Operator_Start_ Rising_Edge R_TRIG FALSE #Operator_Start_ Button FALSE Operater Has Request A Start FALSE True --- #Start System Is Running EN Q #System_Run - 3 CLK ENO



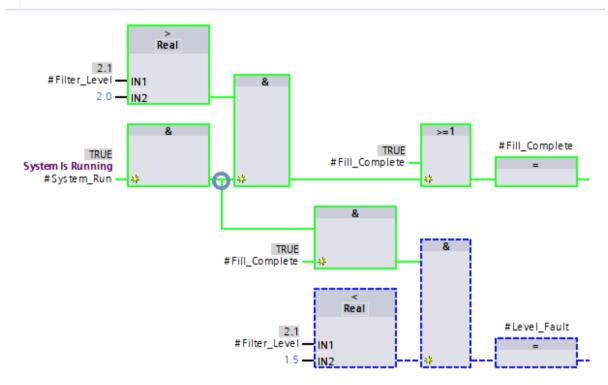


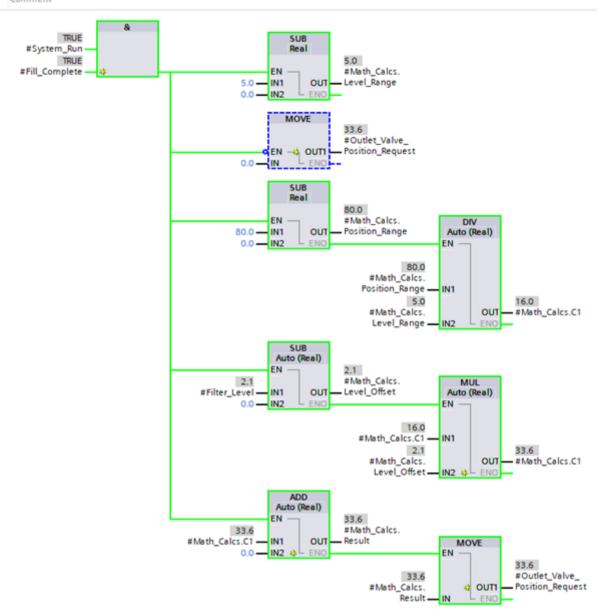
▼ Network 4: System Running - Open Inlet Valve

Comment



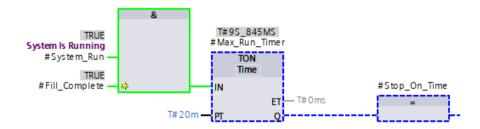
▼ Network 5: System Running - Manage Fill



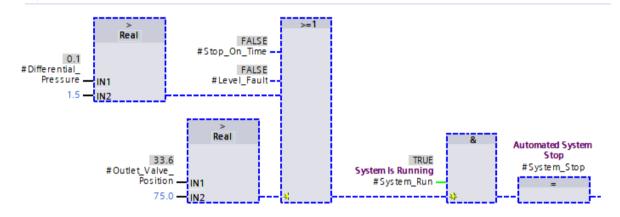


▼ Network 7: System Running - Calculate Time Running

Comment



▼ Network 8: System Running - Automatic Stop



```
IF #Condition = TRUE THEN
    //Condition = True Code
    #ConditionResult := True;
ELSE
    //Condition = False Code
    #ConditionResult := False;
END_IF;
```

∨ Basic instructions		
Name	Description	Version
▼ 🔄 Bit logic operations		V1.0
■ R_TRIG	Detect positive signal edge	V1.0
F_TRIG	Detect negative signal edge	V1.0

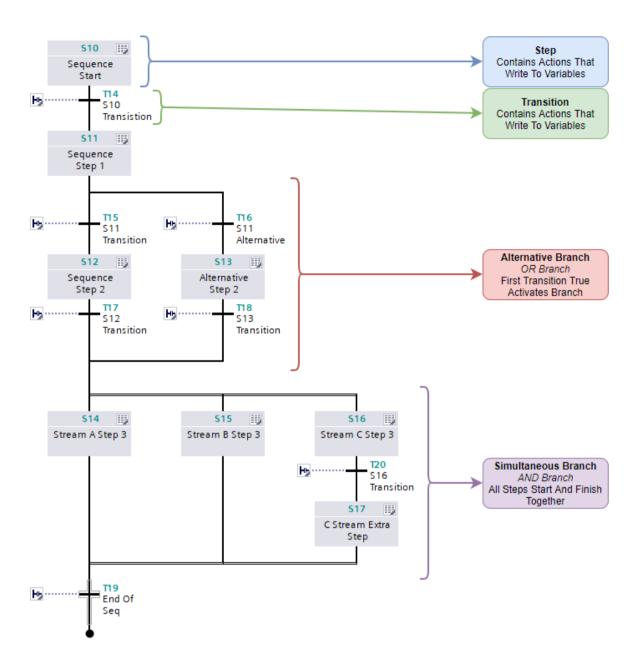
1	<pre>#Condition_1 := TRUE;</pre>			#Condition_1	TRUE
2	#Condition_2 := True;			#Condition_2	TRUE
3					
4	#Result_1 := #Result_2:=	<pre>#Condition_1 AND #Condition_2;</pre>	•	#Result_1	TRUE
				#Result_2	TRUE
				#Condition_1	TRUE
				#Condition_2	TRUE

```
1 #Condition 1 := TRUE;
                                     #Condition_1
                                                         TRUE
2 #Condition_2 := True;
                                     #Condition_2
                                                         TRUE
3
                                     #Result 2
4 #Result 2 := #Condition 1 AND #Condition 2;
                                                         TRUE
5  #Result 1 := #Result 2;
                                     #Result 1
                                                         TRUE
       #Result := NORM X(MIN:= int in , VALUE:= int in , MAX:= int in )
        #Result := NORM X(MIN := 0, VALUE := #Variable 1, MAX := 100);
                     #Result := #Variable 1 >= 20;
              1 // //=====\\
              2 // || SCL - Structured Control Language ||
              3 // |]======[|
              4 // || (Structured Text)
              5 // \\========//
  8 // //-----\\
  9 // || Detect Start
  10 // |]========[|
  11 // || As long as conditions are within limits, and the operator requests ||
  12 // || a start, start the system
  13 // \\=========//
 14
 15
  16 □REGION Detect Start
     //Get Start Conditions From Assets
      #Start_Conditions_OK :=
#Inlet_Valve_Closed AND
 18
 19
  20
      #Outlet Valve Position < 0.5 AND
  21
       #Differential Pressure <= 0.8;</pre>
  22
  23
       //Set Start Condition On Operator Press
  2.4
       #System Run :=
       (#Start Conditions OK AND #Operator Start Button AND NOT #System Run) OR
       (#System Run AND NOT #Operator Start Button AND NOT #System Stop);
  26
  27
  28
       //Reset Operator Button Press
  29
       #Operator_Start_Button := False;
```

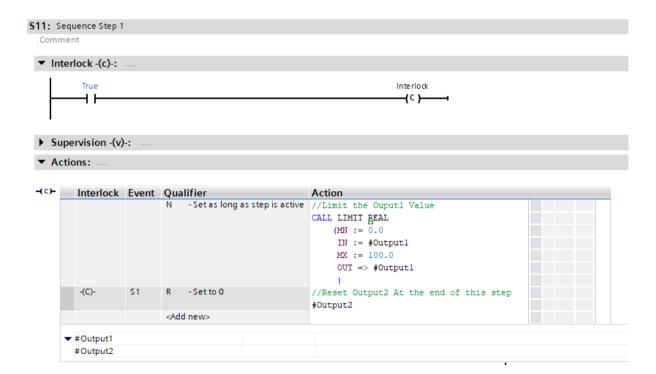
30 END_REGION

```
32 // //=========\\
                     Open Inlet Valve
         33 // ||
         34 // |]=======[|
         35 // || When the system is running, open the Inlet Valve ||
         36 // \\========//
         37
         38 PREGION Open Inlet Valve
            //Open the Inlet Valve
              #Inlet Valve Open Request := #System Run;
         41 END REGION
 43 // //============\\
 44 // || Manage System Fill
 46 // || On first system start, filling is required. If the level is below 2.0 ||
 47 // || the Outlet Valve control may NOT start
 48 // \\========//
 50 □REGION Manage System Fill
 51 //Set Fill Complete
 52
      #Fill Complete :=
 53
      (#System_Run AND #Filter_Level > 2.0) OR
 54
      #Fill Complete AND #System Run;
 55
      //Monitor Level Fault (Low Level When In System Run)
      #Level_Fault := #Filter_Level < 1.5 AND #Fill_Complete;</pre>
 58 END REGION
60 // //-----\\
61 // || Manage Outlet Valve Position ||
62 // |]======[|
63 // || The system is now filled and in System Run Mode, Calculate the ||
64 // || Outlet Valve Position
65 // \\========//
66
67 FIREGION Calculate Outlet Valve Position
68
   //Calculate Outlet Valve Position
69
70
    //((Max Outlet Valve Position - Min Oulet Valve Position)
   //(-----* (Filter Level - Min Filter Level)) + Min Valve Position
//( (Max Filter Level - Min Filter Level)
71
72
73
   #Outlet Valve Position Request := (((80.0 - 0.0) / ( 5.0 - 0.0)) * (#Filter Level - 0.0)) + 0.0;
74
   IF NOT #Fill Complete THEN
75 占
76
     #Outlet_Valve_Position_Request := 0.0;
   END_IF;
77
78 END_REGION
```

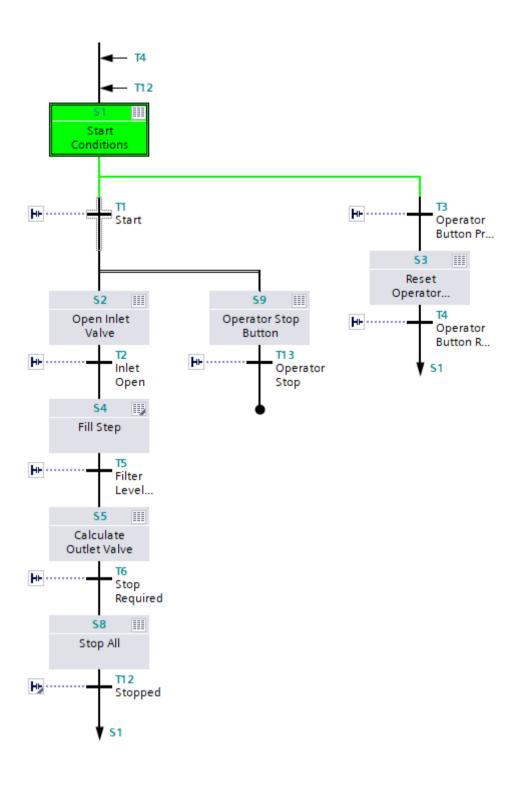
```
80 // //======\\
81 // || Manage Stop Conditions ||
82 // |]======[|
83 // || Conditions that will stop the system ||
84 // \\=======//
86 □REGION Manage Stop Conditions
     //Call Max Run Timer
88 #Max_Run_Timer(IN:=#Fill_Complete,
89
                  PT:=T#20m,
90
                  Q=>#Stop_On_Time);
91
92
     //Manage Stop Conditions
93
     #System Stop :=
94
      (#Stop_On_Time OR
95
     #Level Fault OR
96
     #Differential_Pressure > 1.5 OR
97
      #Outlet Valve Position > 75.0) AND
98
      #System_Run;
99
100 END_REGION
```



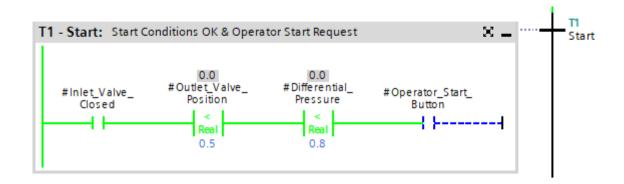
∨ Basic instructions		
Name	Description	Version
▼ ☐ GRAPH LAD instructions		
▶ ☐ General		
 All Bit logic operations 		
▶ Comparator operations		
▼ 🛅 GRAPH actions		
 Timer operations 		V1.0
 +1 Counter operations 		V1.0
 ± Math functions 		V1.0
 Move operations 		<u>V2.5</u>
 Conversion operations 		
▶ 🔐 Program control oper		V1.1
 Word logic operations 		V1.4
Email: Shift and rotate		
▶ Ēīc Legacy		V2.6



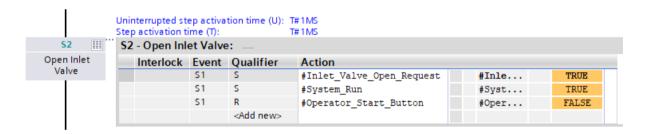




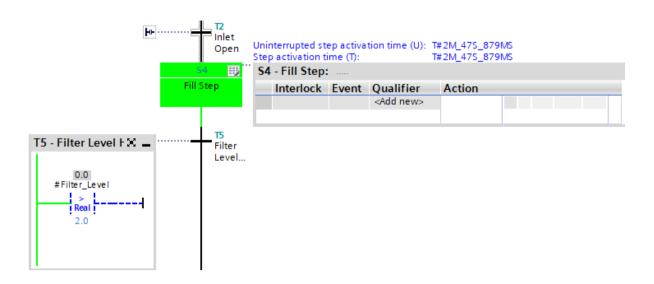


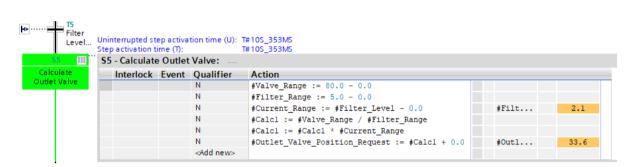


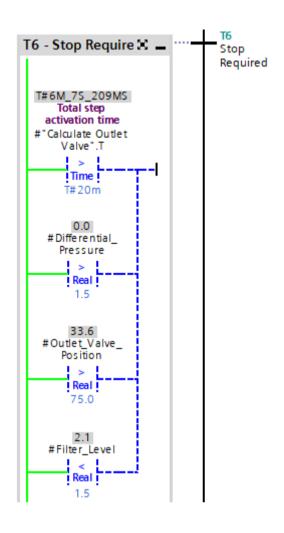




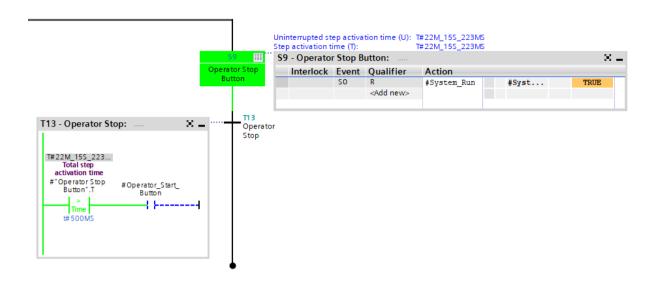


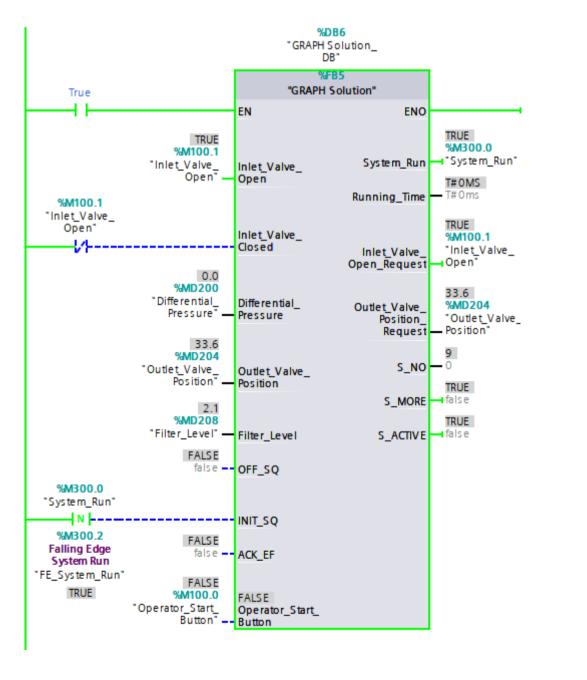


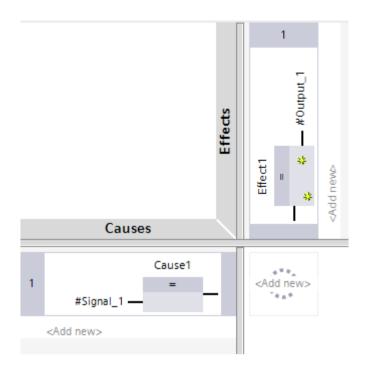


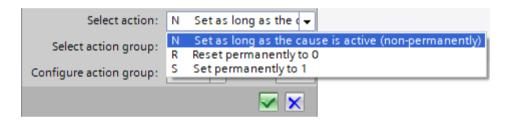




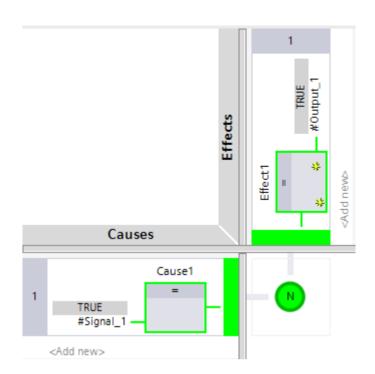


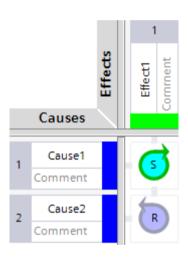


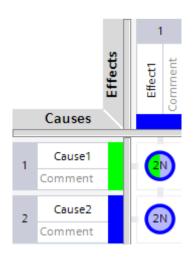


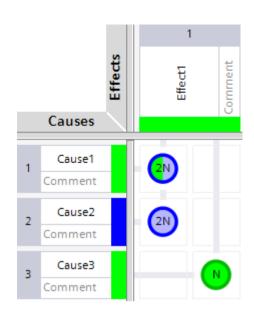


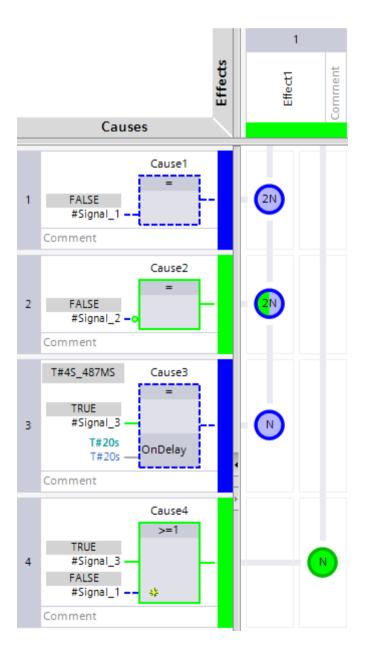
▼ Basic instructions		
Name	Description	Version
▼ 🛅 General		
Empty box	Empty box [F8]	
- 1	Add input/output [Ctrl+Shift+3]	
-0	Invert pin [Ctrl+Shift+4]	
▼ Cause instructions		
Bit logic operations		
□ -[=]	Assignment [Shift+F7]	
₽ &	AND logic operation [F9]	
= >=1	OR logic operation [F10]	
 x	EXCLUSIVE OR logic operation	
Comparator operation	ns	
E CMP ==	Equal	
■ CMP <>	Not equal	
E CMP >=	Greater or equal	
E CMP <=	Less or equal	
■ CMP >	Greater than	
■ CMP <	Less than	
Timer operations		
OnDelay	Delayactivation	
OffDelay	Delay deactivation	
Pulse	Activate for a limited time	
▼ Effect instructions		
= -[=]	Assignment [Shift+F7]	
[S]	Set output	
[₽] -[R]	Reset output	
▼ Intersection actions		
ΦN	Set as long as the cause is active	
	Set permanently to 1	
	Set permanently to 0	



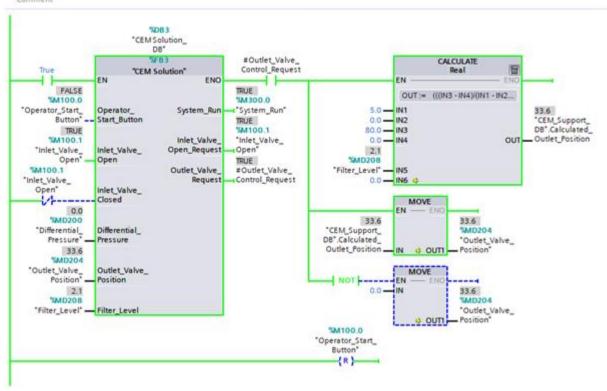


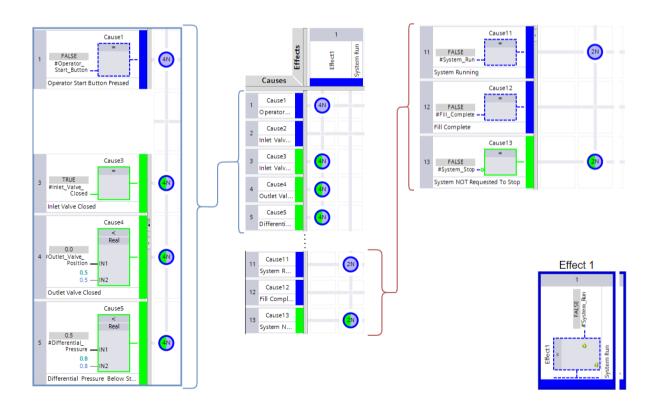


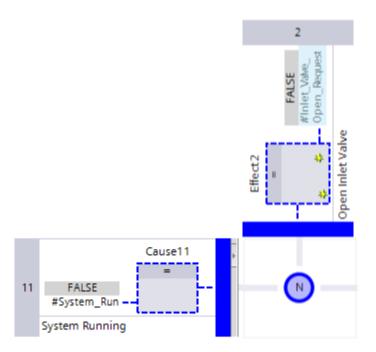


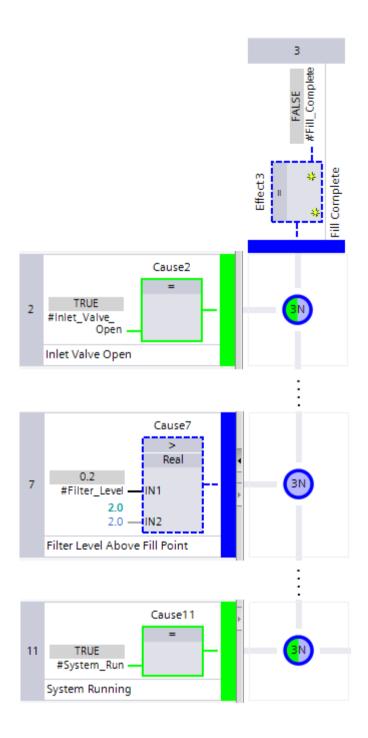


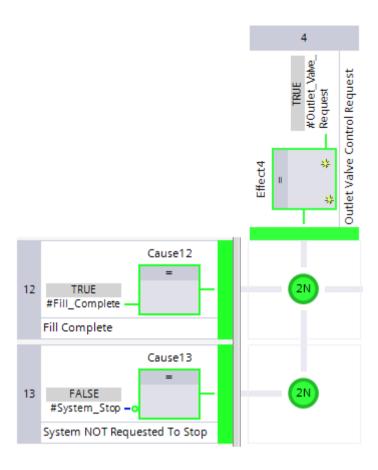


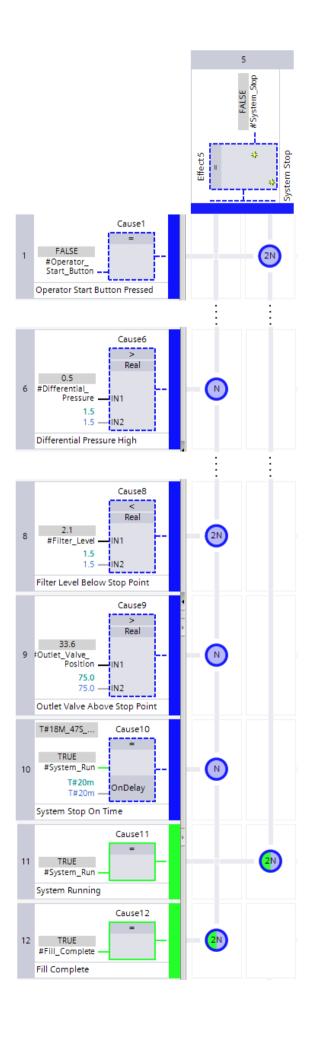




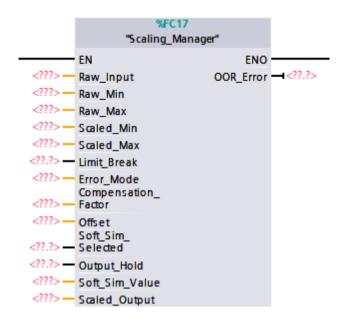




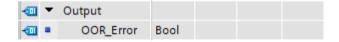




Chapter 6: Creating Standard Control Objects







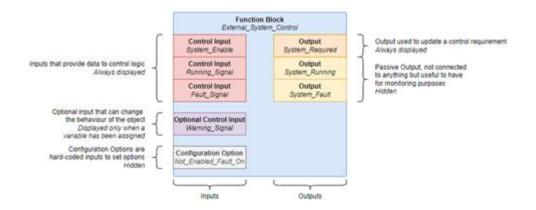
15	1	•	InOut			
16	€11	•	Soft_Sim_Value	Real		
17	€00	•	Scaled_Output	Real		

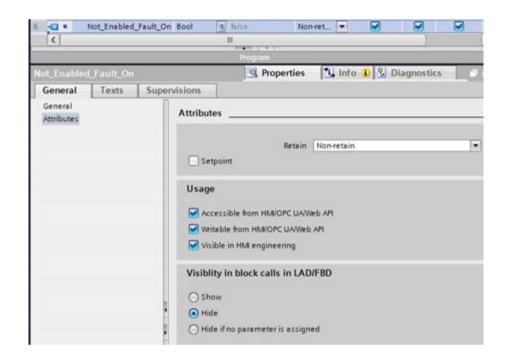
	Copy_Example							
		Na	me	Data type		Offset		
1	1	•	Input					
2	1	٠	Data_In	Array[03999] of LReal		0.0		
3	1	•	Output					
4	1	٠	Data_Out	Array[03999] of LReal		32000.0		
5	1	•	InOut					
6		•	<add new=""></add>					

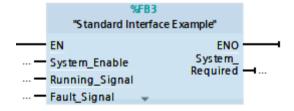
```
1 □FOR #i := 0 TO 3999 BY 1 DO
2  #Data[#i] := #Data[#i] + 5;
3  END_FOR;
```

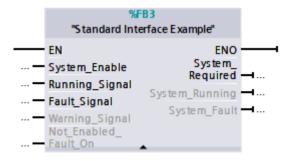
	Pointer_Example								
		Na	me	Data type	Offset				
1	1	•	Input						
2		•	<add new=""></add>						
3	1	•	Output						
4		•	<add new=""></add>						
5	1	•	InOut						
6	1	•	▶ Data	Array[03999] of L	0.0				
7	400	•	Example	Bool	6.0				

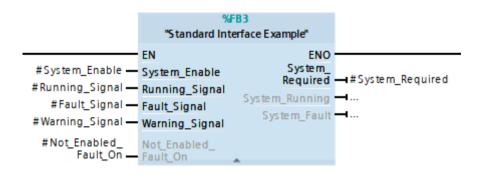
	Resources of PLC_1								
	Objects	Load memory	Code work-memory	Data work-memory					
1		6 %	0 %	3 %					
2									
3	Total:	2 MB	512000 bytes	3145728 bytes					
4	Used:	118350 bytes	902 bytes	96212 bytes					
5	Details								
6	▼ OB	6967 bytes	509 bytes						
7	■ Main [OB1]	6967 bytes	509 bytes						
8	FC	-	-						
9	▼ FB	10322 bytes	393 bytes						
10	Copy_Example [FB1]	5306 bytes	205 bytes						
11	Pointer_Example [FB2]	5016 bytes	188 bytes						
12	▼ DB	99816 bytes		96212 bytes					
13	Copy_Example_DB [DB1]	65359 bytes		64068 bytes					
14	😈 Datablock [DB2]	33175 bytes		32070 bytes					
15	Pointer_Example_DB [DB	1282 bytes		74 bytes					

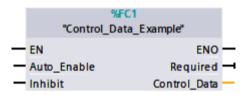




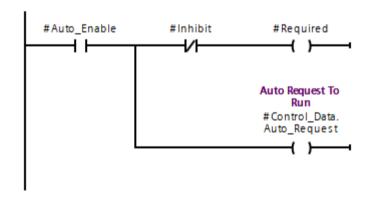


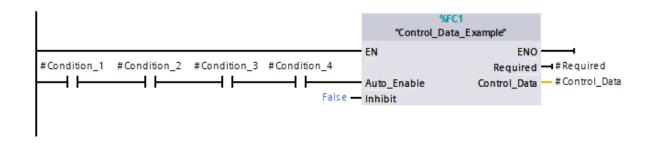


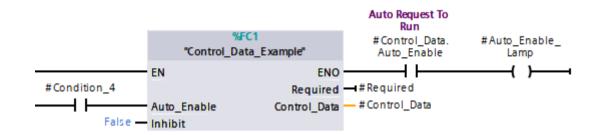


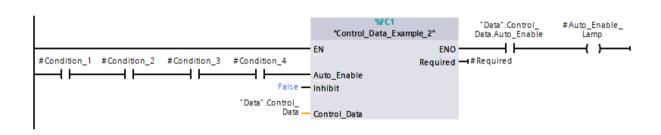


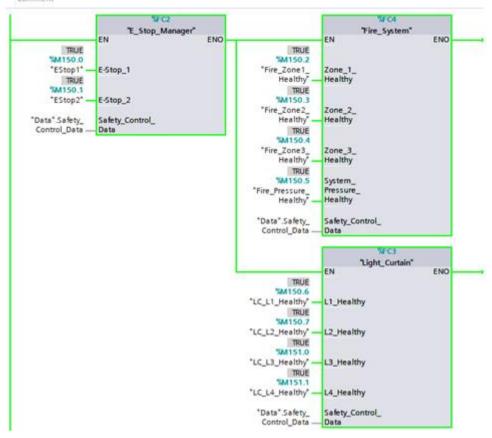
Co	Control_Data_Example									
	Nam	e	Data type	Default value	Comment					
1	– (Output								
1	•	Required	Bool							
1		Control_Data	Struct							
1		Healthy	Bool		Any Fault Active					
1		Not_Available	Bool		Pump Not Available For Any Reason					
4 ■		Auto_Enable	Bool		Pump Is Enabled To Run In Auto					
400		Inhibited	Bool		Pump Inhibited From Running In Auto					
1		Running	Bool							
1		Running_In_Auto	Bool							





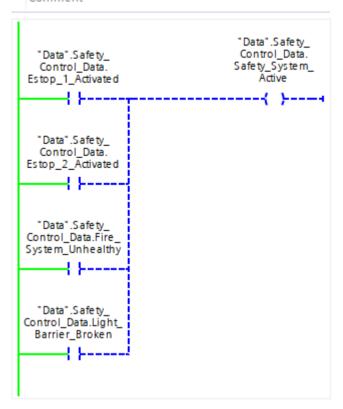




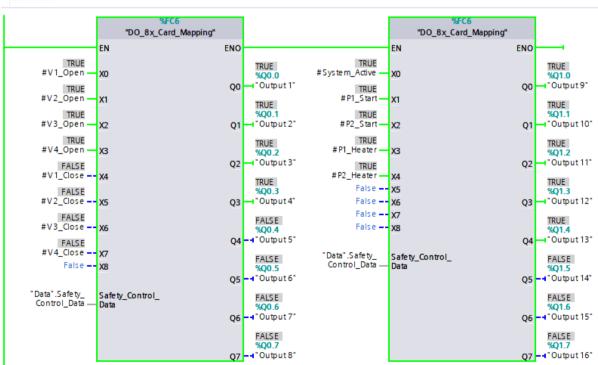


Network 3:

Comment



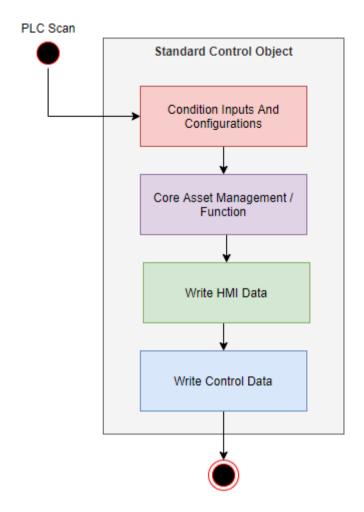
Network 4:

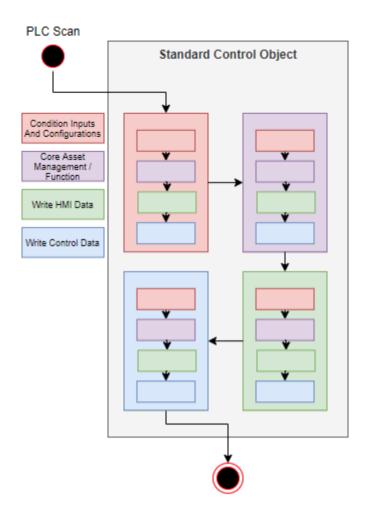


Network 1: Comment #Safe ty_Control_ Data.Safe ty_ Sys te m_Active #X0 #Q0 ----| |------#X1 #Q1 #X2 #Q2 ----- }---------| |------#X4 #Q4 # X5 #Q5 --| }------{ }----{ #Q7 #X7 ---! }-

	UDT_Pump_H MI _Data								
		Name	Data type	Default value	Setpoint	Comment			
1	1	Mimic	USInt	0		0 = Off, 1 = Running In Manual, 2 = Running In Auto			
2	1	Hours_Run	USInt	0		Hours Of Active Service			
3	1	Mode	USInt	0		0 = Off, 1 = Manual, 2 = Auto			

UDT_Level_Controller_HMI_Data								
Name	Data type	Default value	Comment					
Mimic	USInt	0	0 = Healthy, 1 = Faulty (Unaccepted), 2 = Faulty (Acc					
Percentage	Real	0.0	Level As %					
Level_Status	USInt	0	0 = Healthy, 1 = Low Low, 2 = Low, 3 = High, 4 = Hig					
Setpoints	Struct		Standard Setpoints					
 ■ Normal_Level 	Real	50.0						
LowLow_Level	Real	10.0						
 ■ Low_Level 	Real	15.0						
■ High_Level	Real	80.0						
HighHigh_Level	Real	95.0						
■ Hysteresis	Real	5.0						

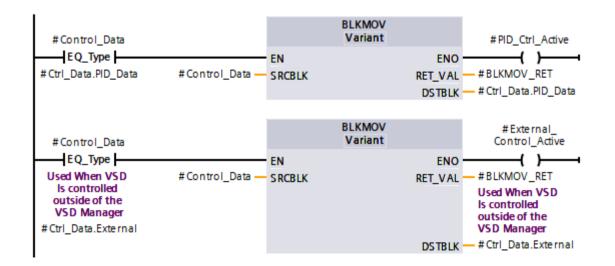




Sca	Scaling_Manager							
	Na	me	Data type	Offset	Default value	Comment		
1	•	Input						
1	•	Raw_Input	Int					
€11	•	Raw_Min	Int					
€11	•	Raw_Max	Int					
€11		Scaled_Min	Real					
€11		Scaled_Max	Real					
€11	•	Limit_Break	Bool			Allows the scaled value to breach min / max limits		
€11	•	Error_Mode	Int			0 = Last Known, 1 = Force High, 2 = Force Low		
4 □	•	Compensation_Factor	Real					
4 □	•	Offset	Real					
4 □	•	Soft_Sim_Selected	Bool					
4 □	•	Output_Hold	Bool					
4 □	•	Output	<u> </u>					
€00	•	OOR_Error	Bool					
4 □	InOut							
4 □	•	Soft_Sim_Value	Real					
1		Scaled_Output	Real					

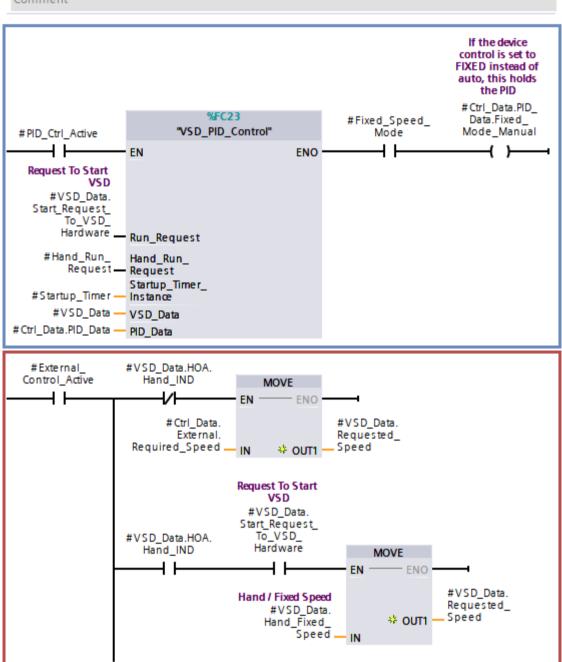
1	•	InC	Out			
1	•	•	Generic_Analog_Data	"UDT_Generic_Analog_HMI_Values"	32.0	Generic Analog Data Structure
1		•	Raw_Value	Dint	0.0	Analog Raw Value
1		•	Scaled_Value	Real	4.0	Analog Scaled Value
1		•	Trend_Value	Real	8.0	Manipulated Trend Value
1		•	Soft_Sim_Value	Real	12.0	Soft Sim Value From SCADA
1		•	Raw_Min	Int	16.0	Minimum Raw Value
1		•	Raw_Max	Int	18.0	Maximum Raw Value
1		•	Instrument_Min	Real	20.0	Minimum Displayed Instrument Value
1		•	Instrument_Max	Real	24.0	Maximum Displayed Instrument Value
1		•	Scale_Min	Real	28.0	Minum Scaled Value
1		•	Scale_Max	Real	32.0	Maximum Scaled Value
1		•	Offset	Real	36.0	Offset Applied To Scaled Value
1		•	Compenstation_Fa	Real	40.0	Multiplication Factor Applied To Scaled Value
1		•	HiHi	Real	44.0	HiHi Alarm Setpoint
1		•	Hi	Real	48.0	Hi Alarm Setpoint
1		•	Lo	Real	52.0	Lo Alarm Setpoint
1		•	LoLo	Real	56.0	LoLo Alarm Setpoint
1		•	Hysteresis	Real	60.0	Hysteresis
1		•	HiHi_Release	Real	64.0	HiHi Alarm Release
1		•	Hi_Release	Real	68.0	Hi Alarm Release
1		•	Lo_Release	Real	72.0	Lo Alarm Release
1		•	LoLo_Release	Real	76.0	LoLo Alarm Release
1		•	Alarm_IND	Byte	80.0	Instrument Alarm Indication For SCADA
1		•	Fault_IND	Byte	81.0	Fault Indication For SCADA
1		•	Alarm_Trigger_Del		82.0	Alarm Trigger Delay
1		٠	Alarm_Release_Del	Time	86.0	Alarm Release Delay
1		٠	Fail_Delay	Time	90.0	Fail Delay (OOR)
1		٠	OOR_Fault	Bool	94.0	OOR Fault Status (Pre Delay)
1		•	HMI_Tag_Check	Bool	94.1	RESERVED FOR HMI/SCADA
1		•	Alarm_Status	Struct	96.0	

•	InOut		
•	VSD_Data	"UDT_VSD_Drive"	54.0 i4.0
•	Hardware_Data	Variant	
	Control_Data	Variant	

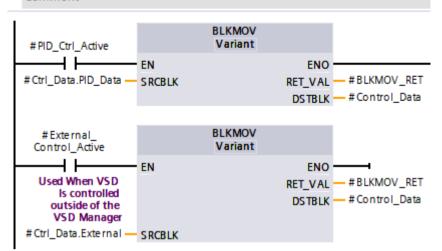


•	•	Cti	rl_Data	Struct	54.0
	•	٠	PID_Data	"UDT_PID"	54.0
		•	External	"UDT_VSD_External	96.0

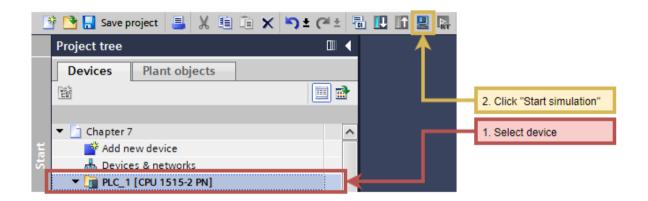
Network 26: Process Control Method

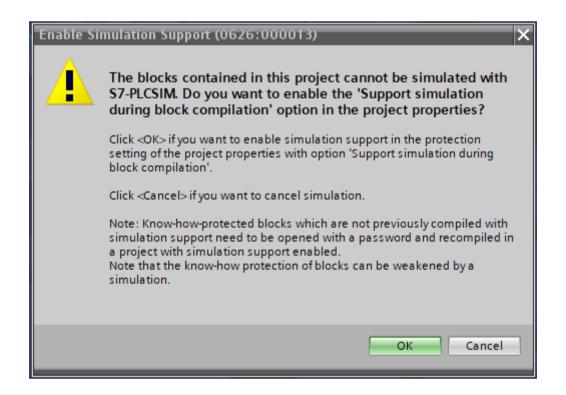


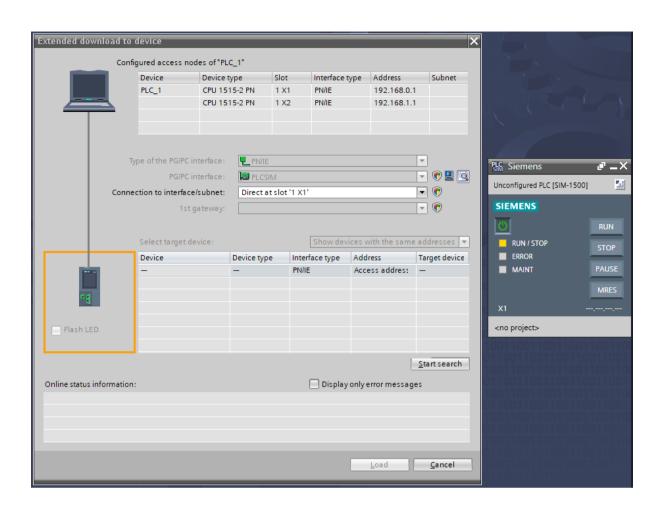
Network 27: Write To Control Data

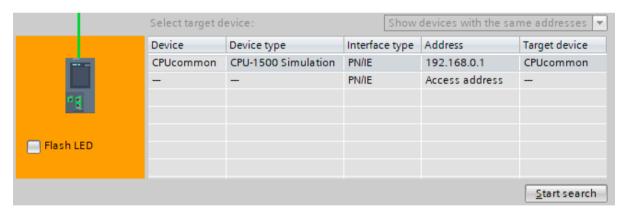


Chapter 7: Simulating Signals in the PLC

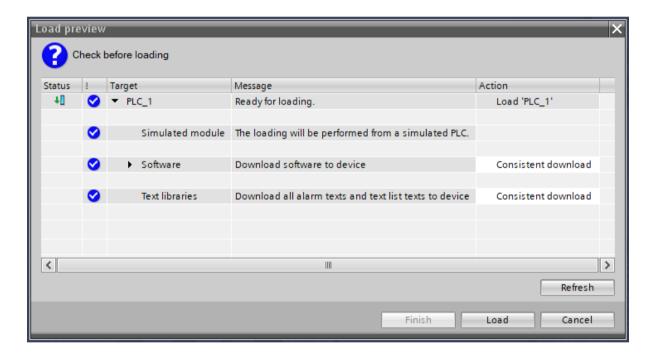


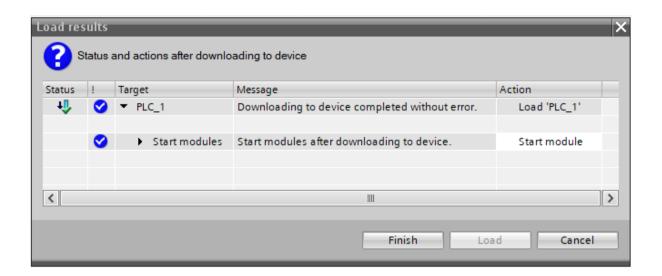


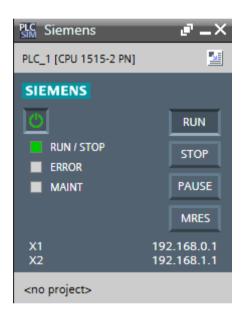


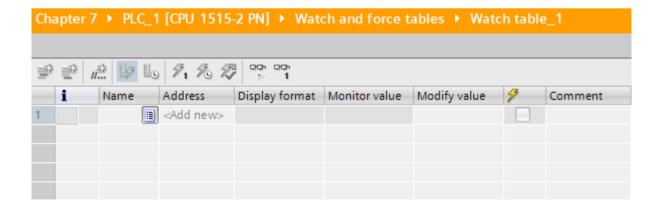






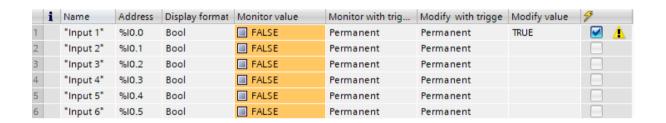




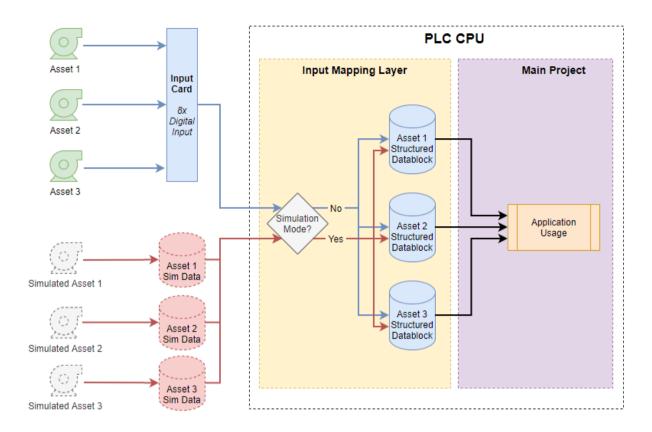


∌	∌ ₽ 1 ₽ 1 9 1 8 2 1 1 1 1 1 1 1 1 1 1								
	i	Name	Address	Display format	Monitor value				
1		"Input 1"	%10.0	Bool	FALSE				
2		"Input 2"	%10.1	Bool	FALSE				
3		"Input 3"	%10.2	Bool	FALSE				
4		"Input 4"	%10.3	Bool	FALSE				
5		"Input 5"	%10.4	Bool	■ FALSE				
6		"Input 6"	%10.5	Bool	■ FALSE				



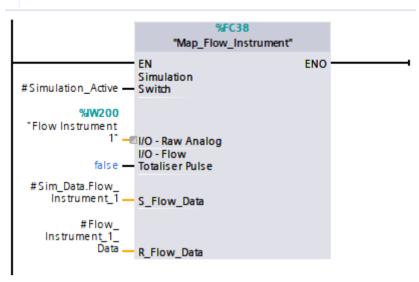


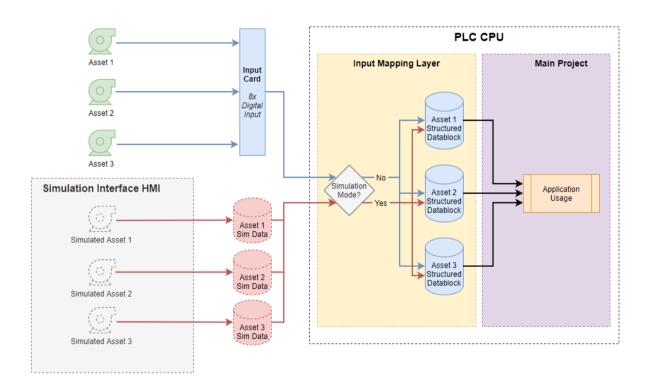




Network 1: Simulation Switch Comment #Simulation_ Pushbutton #Simulation_Active #Simulation_Active -{ }-----//-----()-- #Simulation_ Pushbutton #Simulation_Active \dashv \vdash Network 2: Reset Pushbutton Comment #Simulation_ #Simulation_ Pushbutton Pushbutton Network 3: Asset 1 Comment "Sim_Asset_1". "Asset_1".Input_ Data.Contactor_ Data.Contactor_ Closed Closed #Simulation_Active \dashv \vdash 4 6 **←** }-%10.0 Slot 2 - CHO "Asset_1_ #Simulation_Active Contactor_Closed"

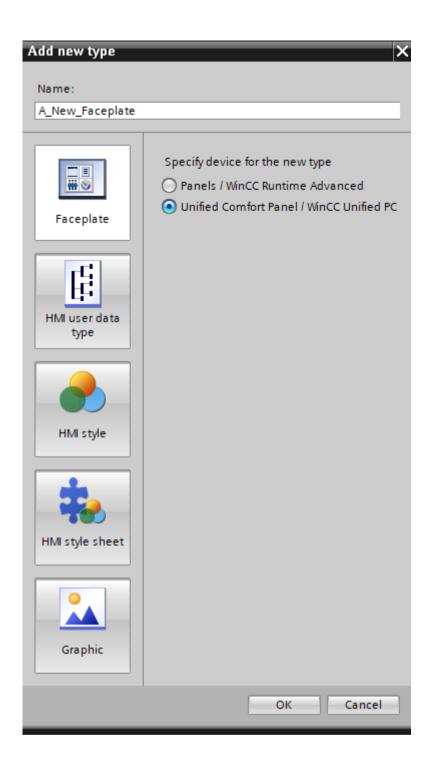
Network 3: Flow Instrument 1 - Mapping / Simulation

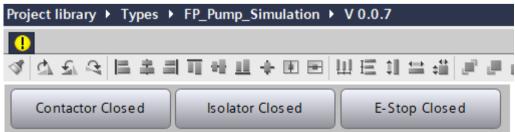




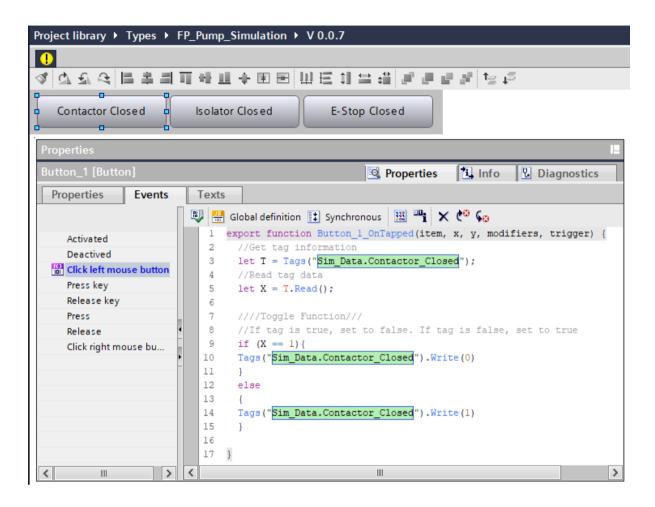
▼ 🛅 HMI_1 [MTP1200 Unified Comfort]
T Device configuration
U Online & diagnostics
Runtime settings
▶ <u></u> Screens
▶ 🔚 HMI tags
🔁 Connections
☑ HMI alarms
Parameter set types
Logs Logs
5 Scheduled tasks
▶ 🖫 Scripts
Collaboration data
🖒 Cycles
Text and graphic lists

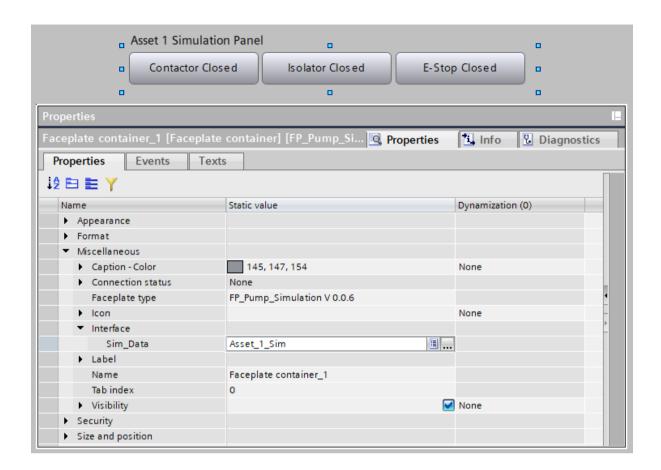
Default tag table							
Name 🔺	Data type	Connection	PLC name	PLC tag			
◆ Asset_1_Sim	UDT_Pump_Input_Data	HMI_Connection_1	PLC_1	Sim_Asset_1.Data			
◆ Asset_2_Sim	UDT_Pump_Input_Data	HMI_Connection_1	PLC_1	Sim_Asset_2.Data			
◆ Asset_3_Sim	UDT_Pump_Input_Data	HMI_Connection_1	PLC_1	Sim_Asset_3.Data			

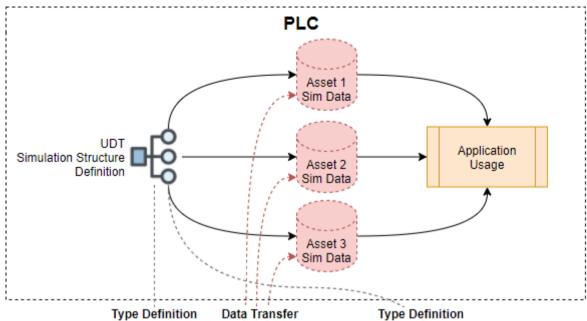


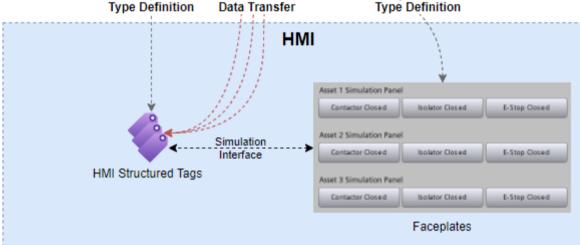


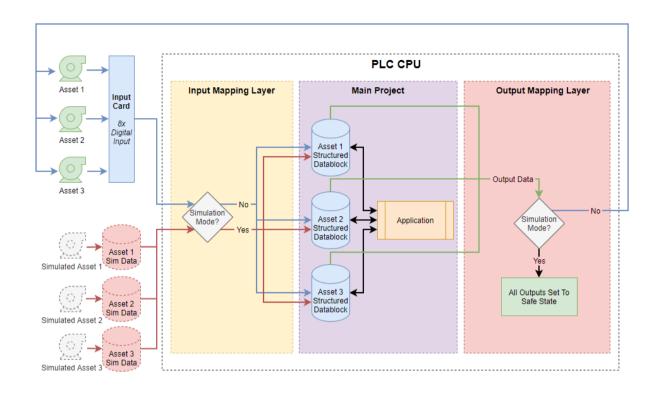
Name	Data type	User data type structure
Sim_Data	Struct	UDT_Pump_Input_Data V 0.0.1





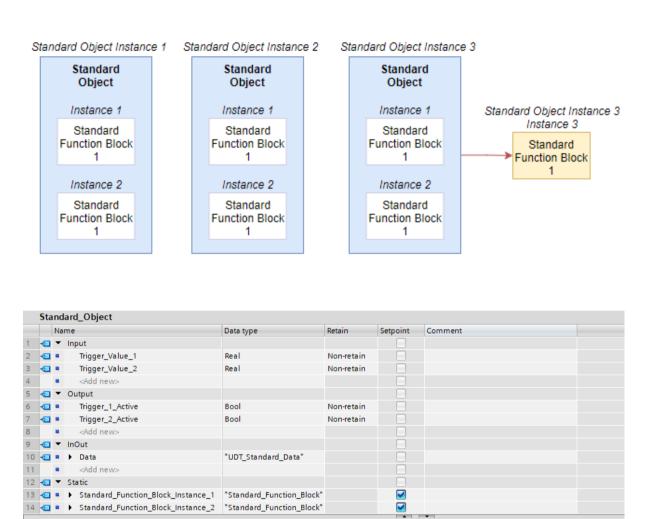


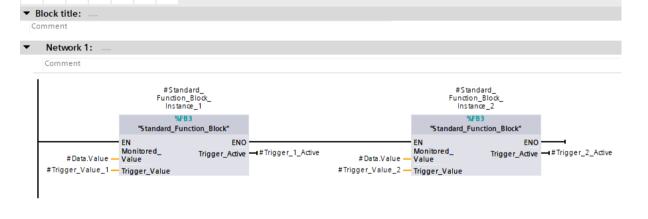


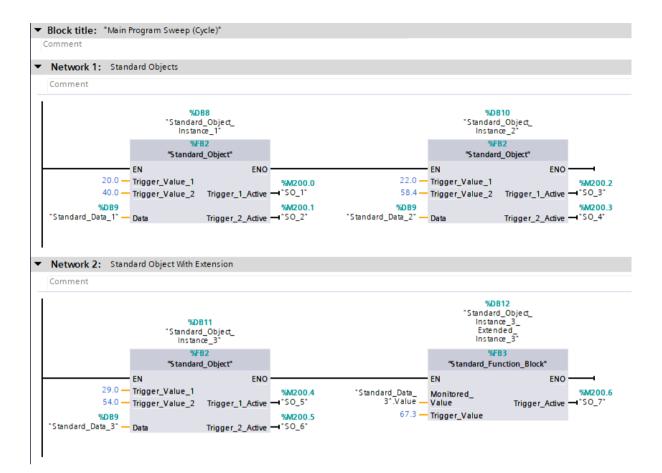


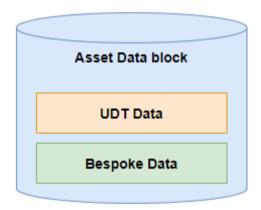
```
1 #Zero := 0;
 2 DIF NOT "H1_HSim_Select".Master_Sim_Sel THEN
         "H1_M1_Node26A_O-1" := "E501".Typed.UV_System.Run_Output_To_UV_System;
"H1_M1_Node11_AO" := "AV302".Typed.POW_Word;
"H1_M1_Node15_AO" := "IF501".Typed.Raw_Value;
 4
 5
 7
         //P501
 8
         #Temp_INT := BLKMOV(SRCBLK := "P501_G120".Typed.Control_Telegram, DSTBLK => P#Q1840.0 Byte 20);
10
11
          //RO Mapping
12
          "Output_Mapping_RlA"();
          "Output_Mapping_R1AN"();
13
14
15
    ELSE
16
17
          #Temp_INT := FILL(BVAL := #Zero, BLK => P#Q0.0 Byte 3000);
18
19
20 END_IF;
```

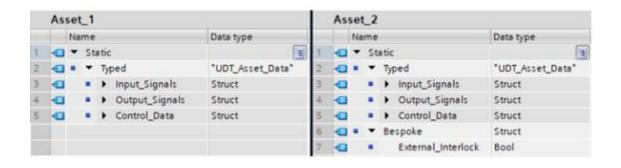
Chapter 8: Options to Consider When Creating PLC Blocks



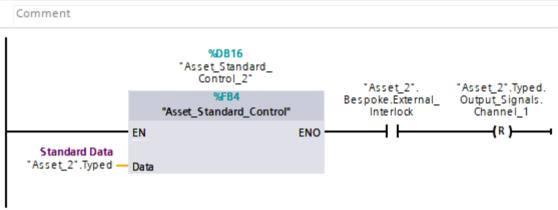


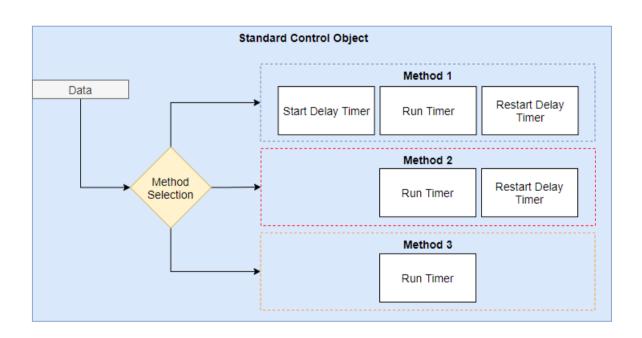


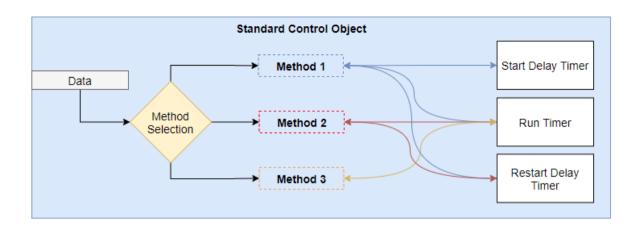




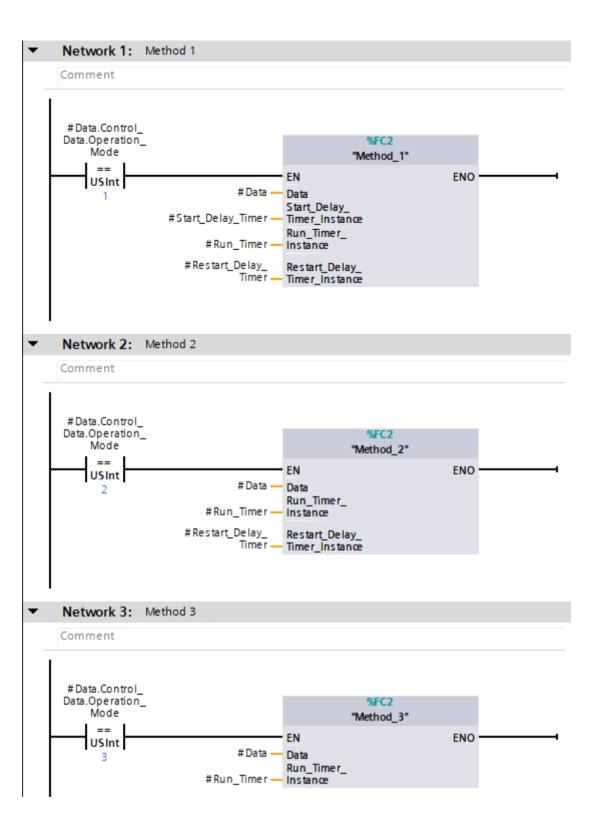
Network 3: Asset 1 - Normal Standard Control & Data Comment **MDB15** "Asset_Standard_Control_1" **SFB4** "Asset_Standard_Control_" EN ENO Standard Data "Asset_1".Typed — Data Network 4: Asset 2 - Extended Standard Control & Data

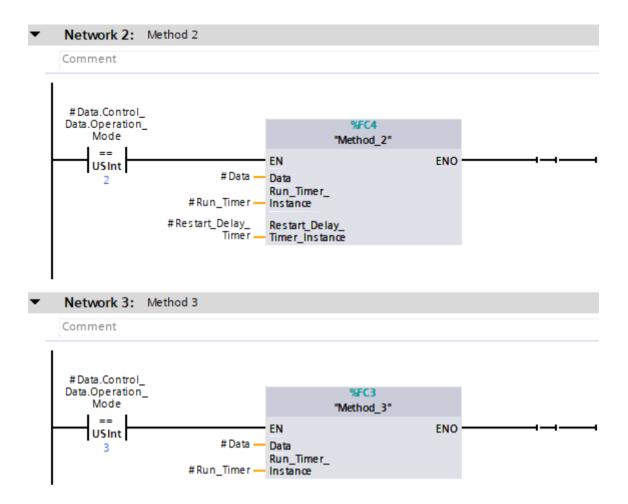


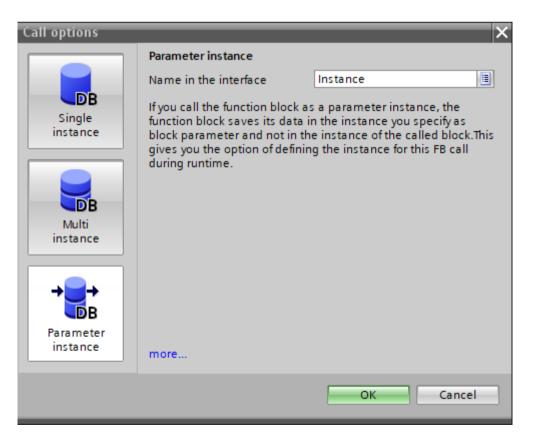


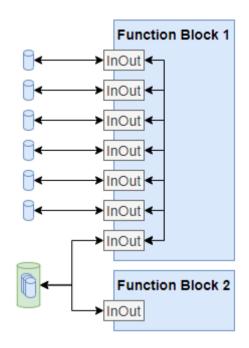


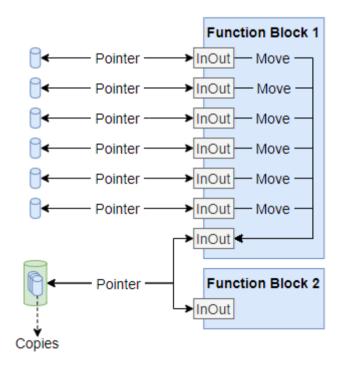
Standard_Control_Object					
	Name		Data type		
1	•	InOut			
1		▶ Data	"UDT_Asset_Data"		
40	•	Static			
1		 Start_Delay_Timer 	TON_TIME		
1	•	Run_Timer	TON_TIME		
1	•	 Restart_Delay_Timer 	TON_TIME		

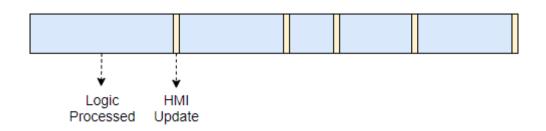


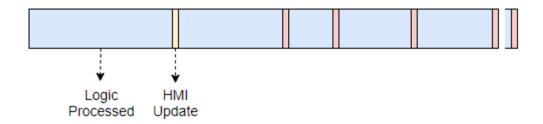


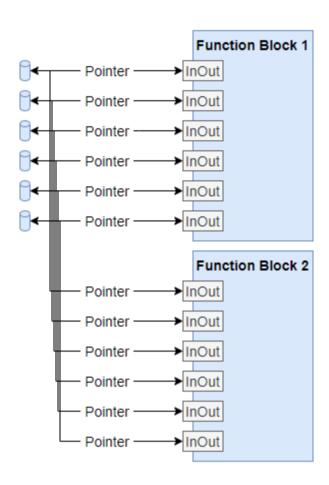




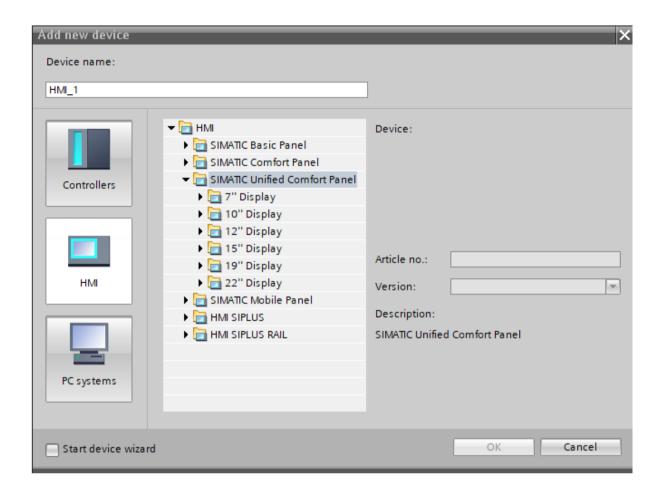


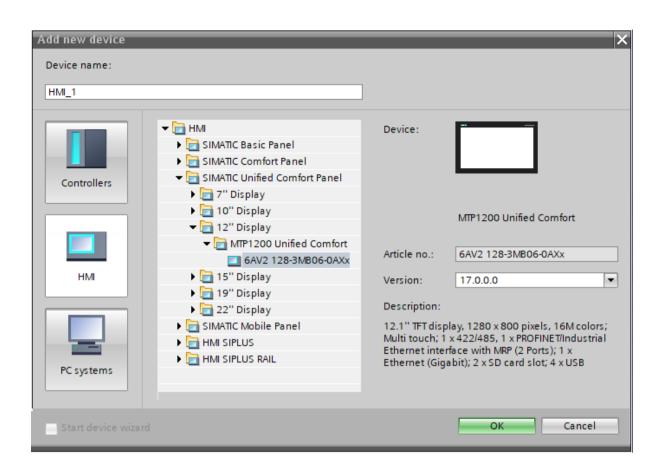




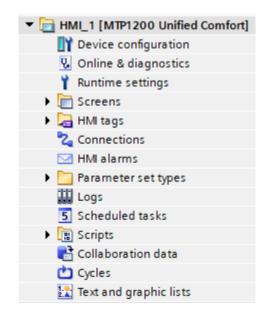


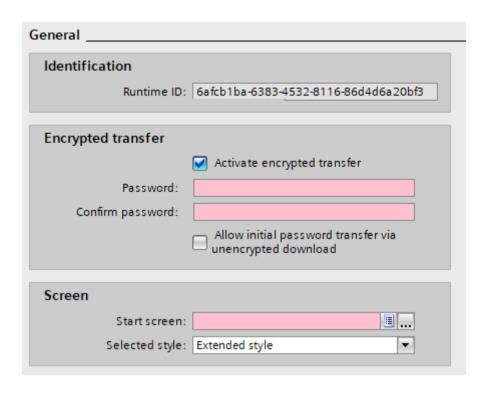
Chapter 9: TIA Portal HMI Development Environment

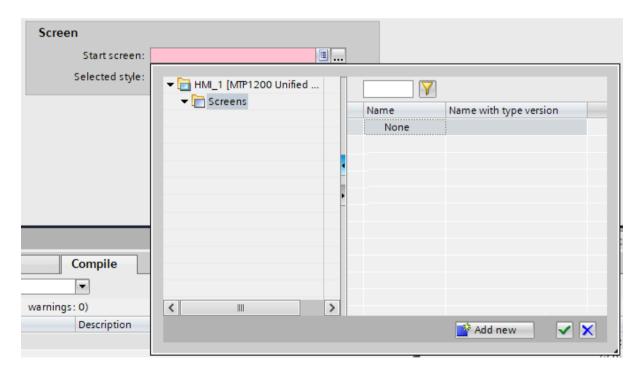




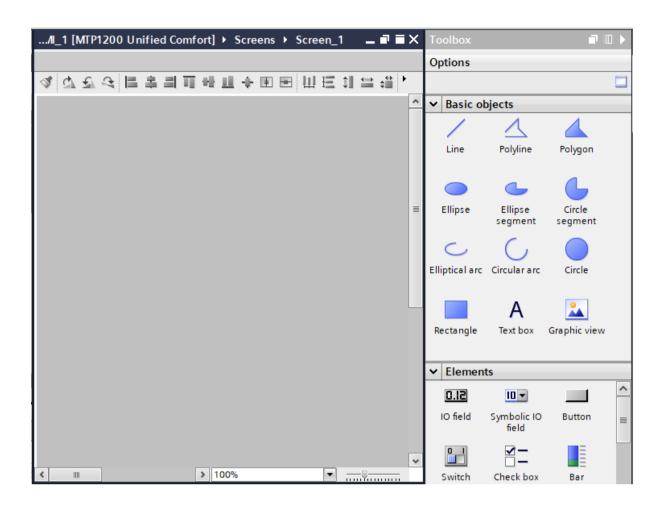


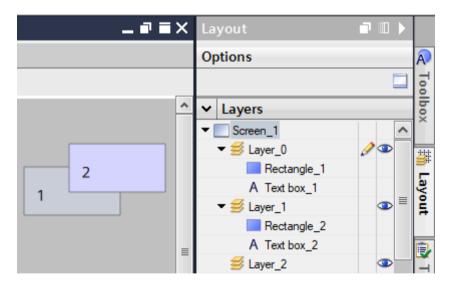


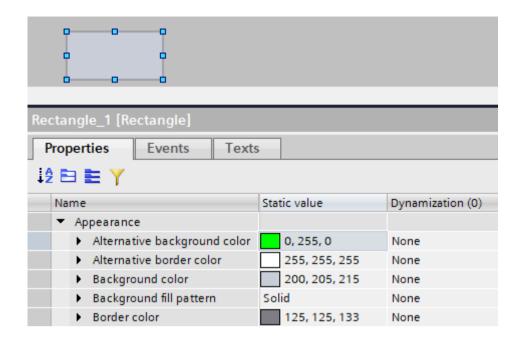


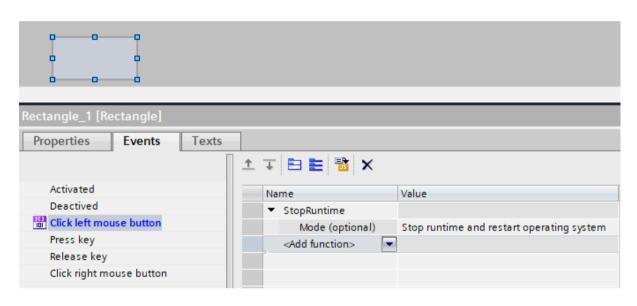


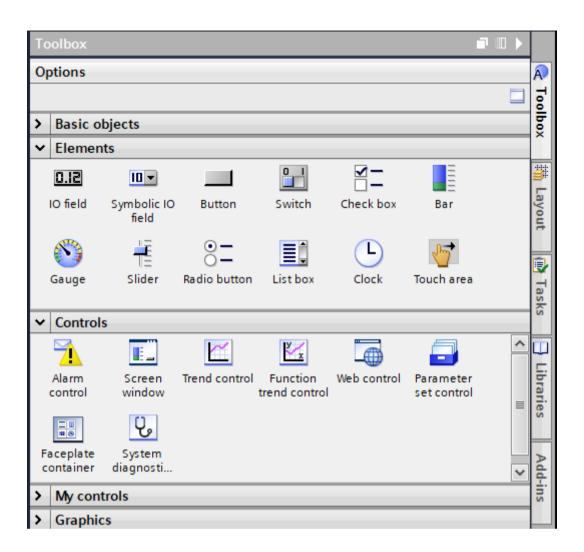


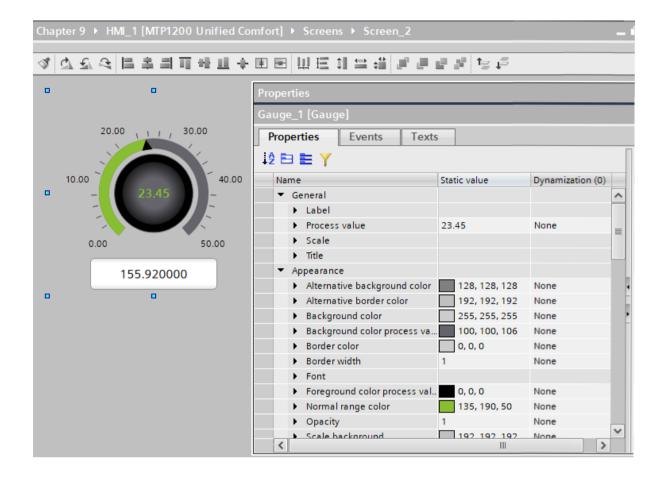


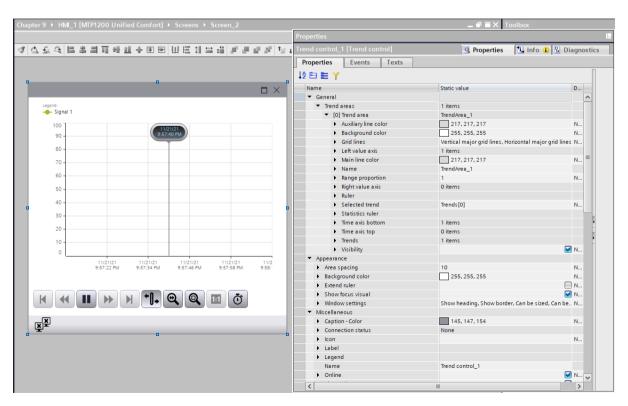


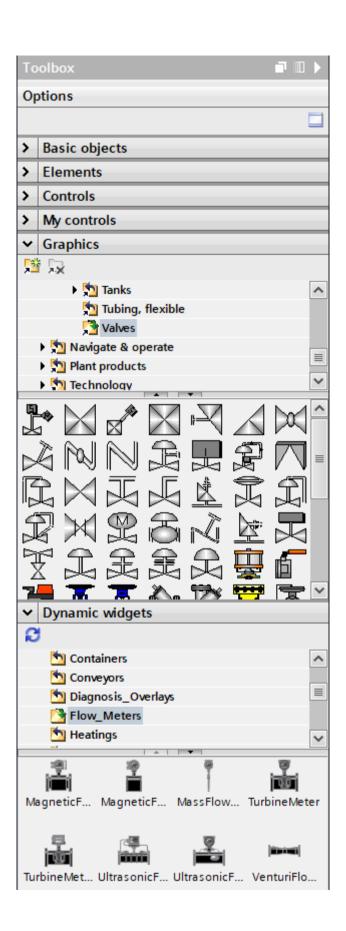




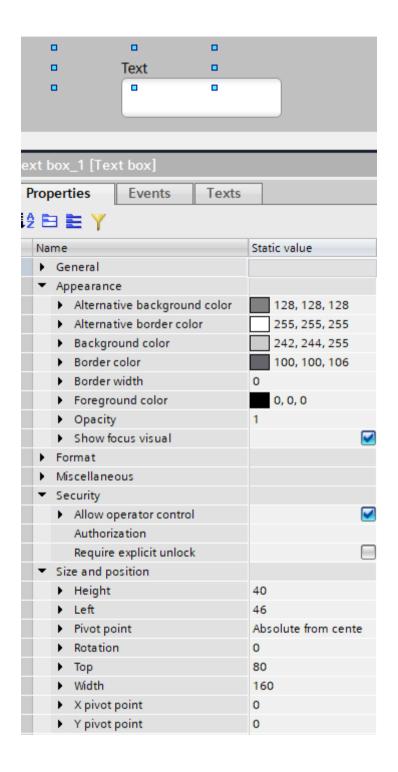


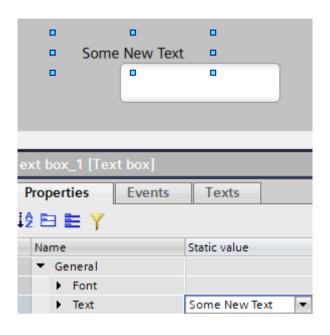


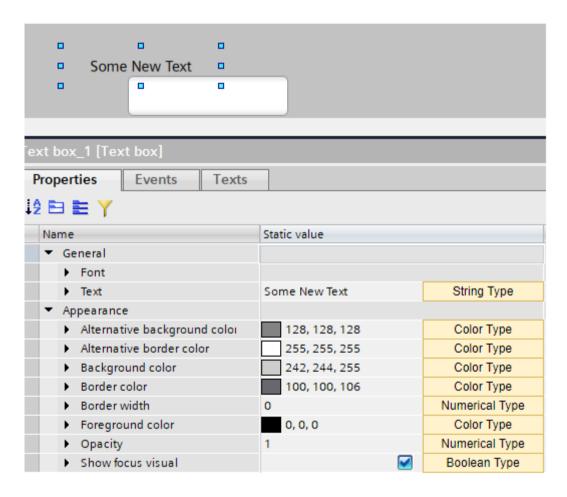


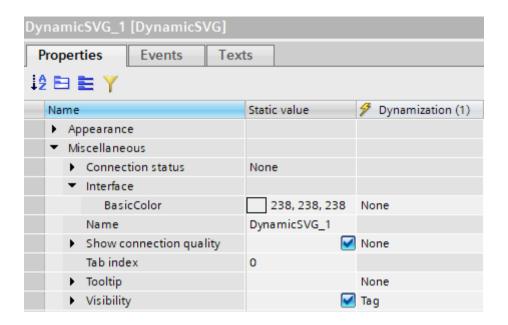


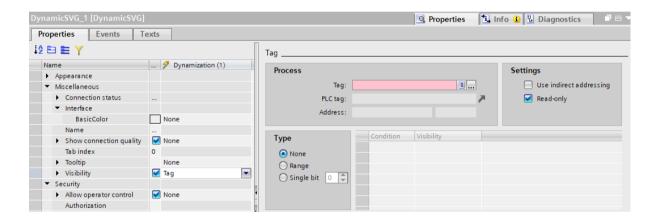
Chapter 10: Placing Objects, Settings Properties, and Events

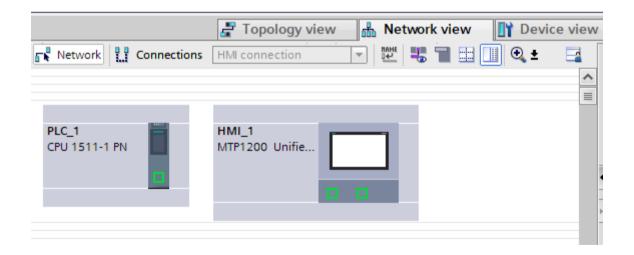


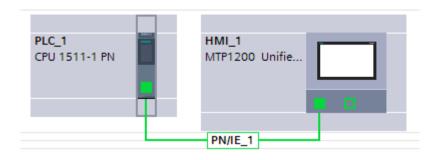


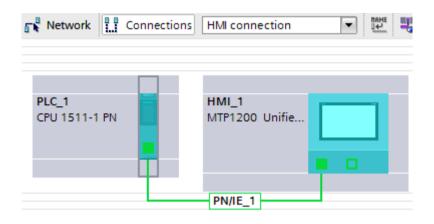


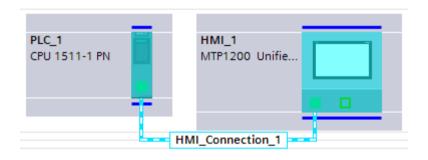


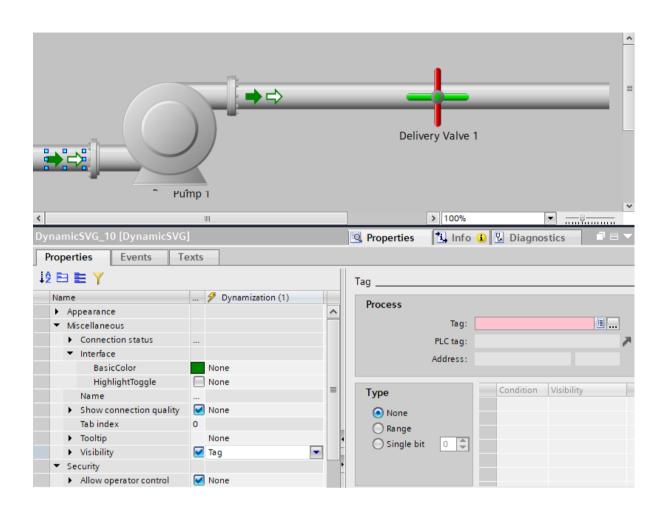


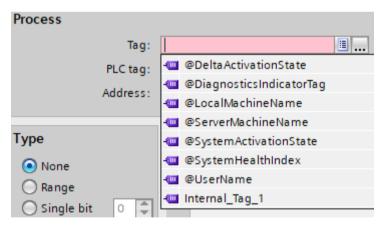


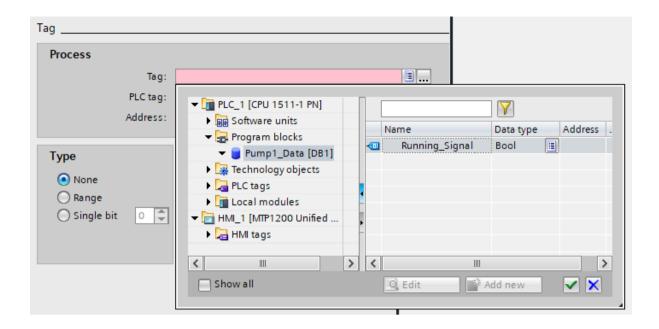


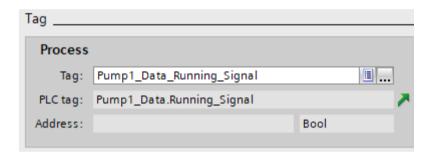


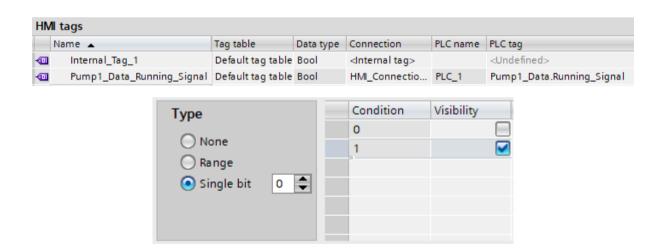


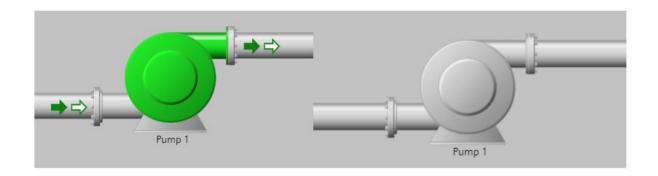


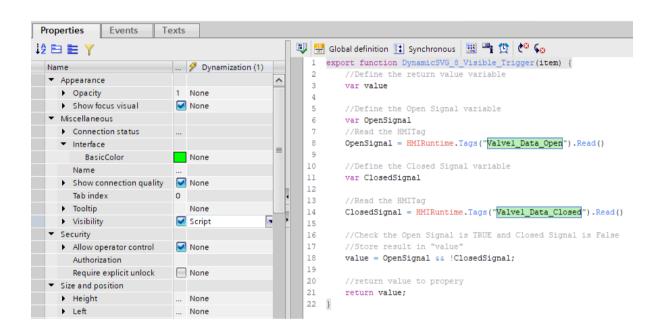








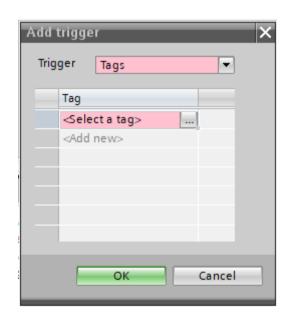


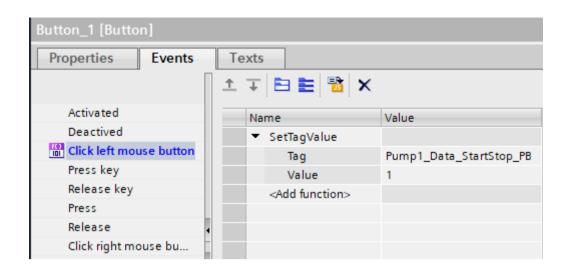


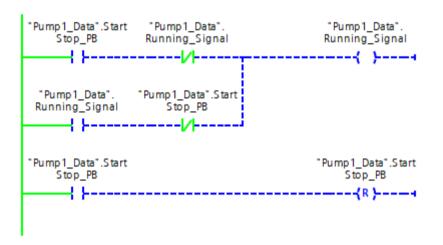
```
export function DynamicSVG_8_ToolTipText_Trigger(item) {
   var value;
   return value;
}
```

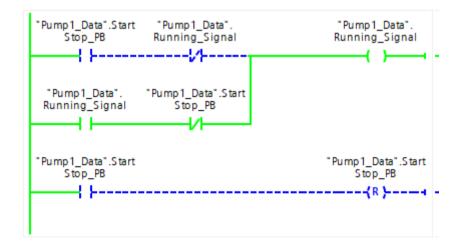
1	<pre>export function DynamicSVG_8_Visible_Trigger(item) {</pre>	Function Declaration - Automatically added
2	//Define the return value variable	Declare Return Value
3	var value	Designe Netalli Value
4 5	//Define the Open Signal variable	
6	var OpenSignal	Declare Tag Signal
7	//Read the HMITag	
8	OpenSignal = HMIRuntime.Tags("Valvel Data Open").Read()	Read Tag Signal
9	opening at the transfer of the	
10	//Define the Closed Signal variable	
11	var ClosedSignal	Declare Tag Signal
12		Boolare ray olynar
13	//Read the HMITag	Read Tag Signal
14	ClosedSignal = HMIRuntime.Tags("Valvel_Data_Closed").Read()	
15 16	((Charle the Ocean Circuit in MINITE and Circuit Circuit in Minite	
17	<pre>//Check the Open Signal is TRUE and Closed Signal is False //Store result in "value"</pre>	Test Acquired Signals
18	value = OpenSignal && !ClosedSignal;	rest Acquired Signals
19	value - OpenSignal && :ClosedSignal;	
20	//return value to propery	
21	return value;	Return value to property
22)	End Of Function Declaration - Automatically added

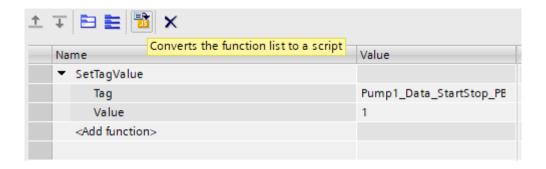






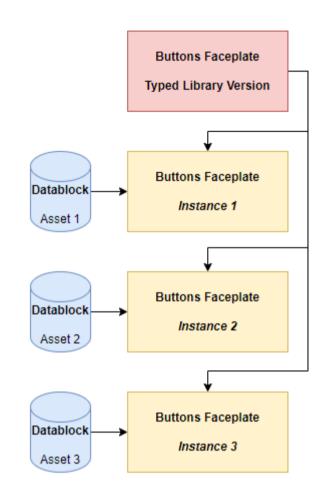




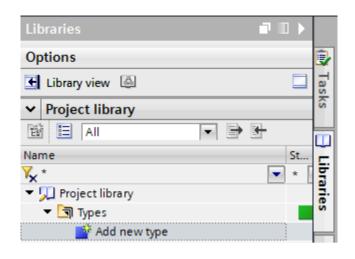


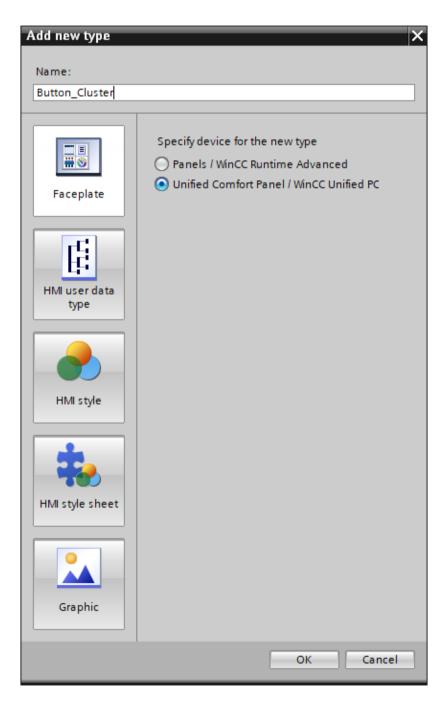
```
1 export async function Button_1_OnTapped(item, x, y, modifiers, trigger) {
2  HMIRuntime.Tags.SysFct.SetTagValue("Pumpl_Data_StartStop_PB", 1);
3
4  }
```

Chapter 11: Structures and HMI Faceplates

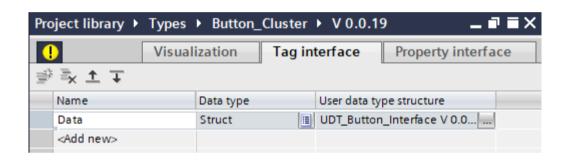


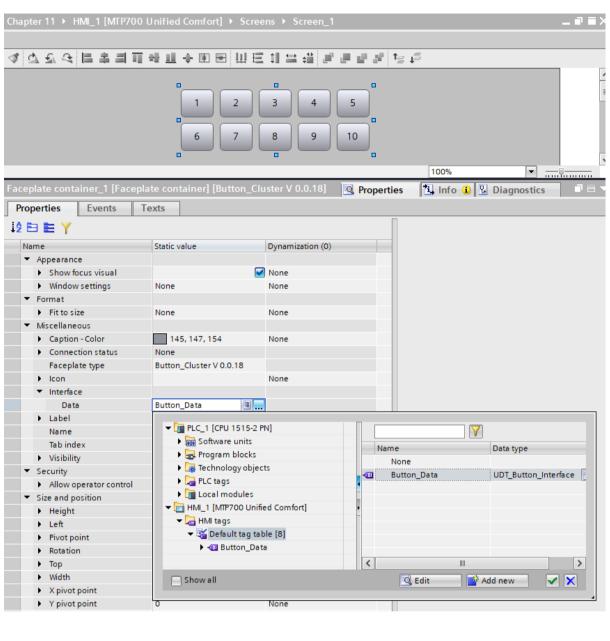


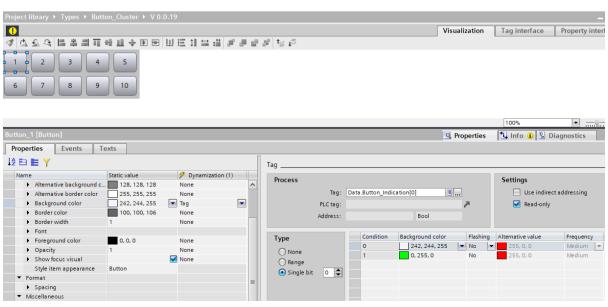


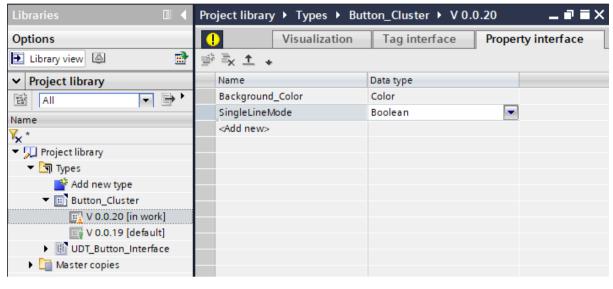


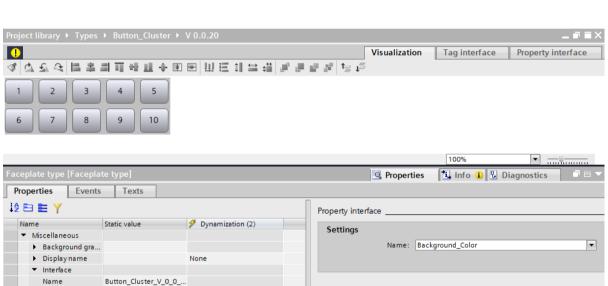












•

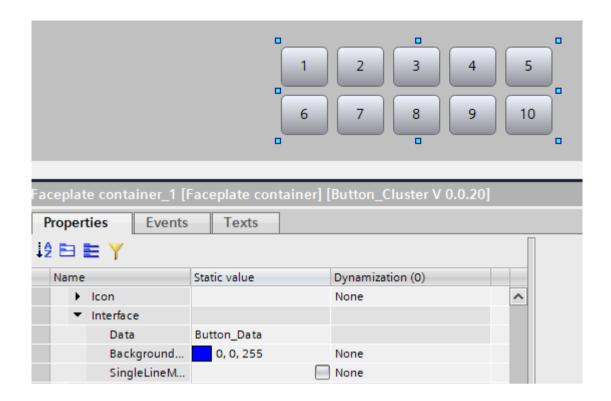
▼ Appearance

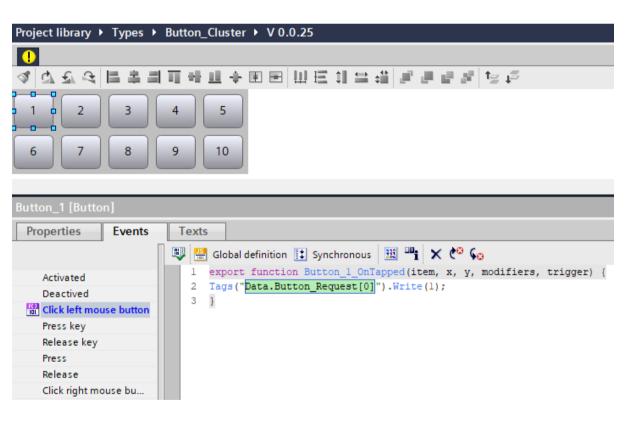
► Alternative back... 235, 235, 235
► Background color 192, 192, 192

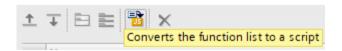
▶ Background fill ... Solid

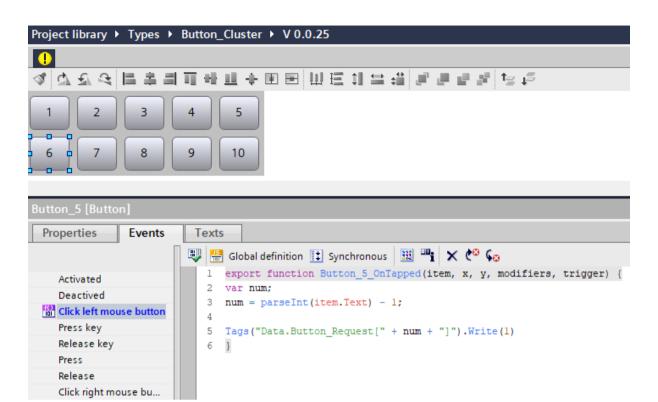
None

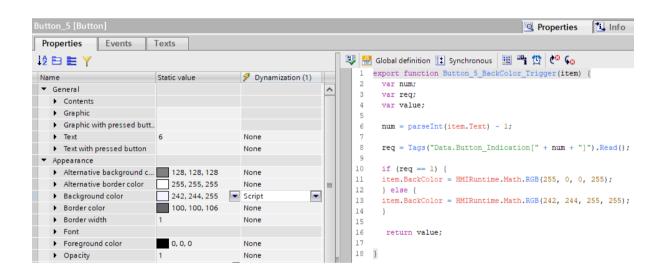
▼ Property interface



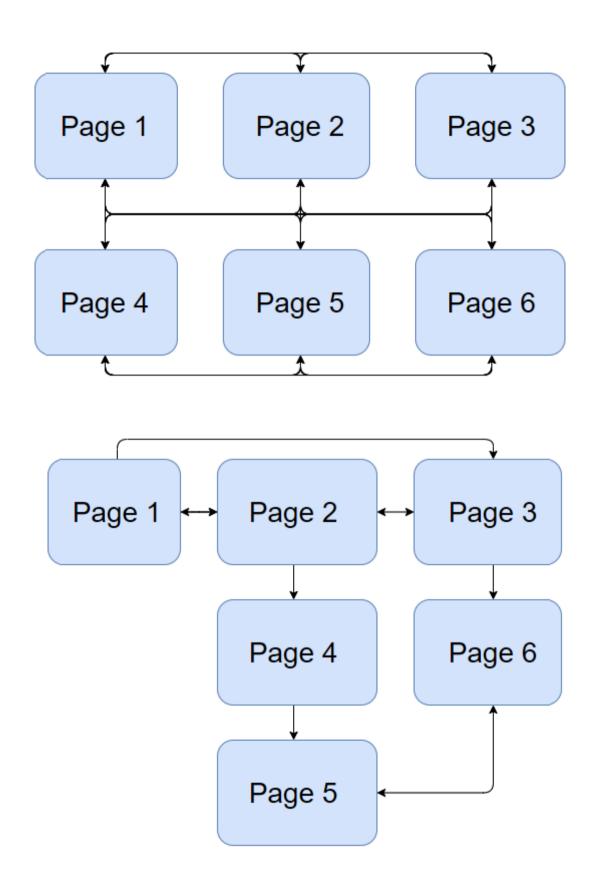


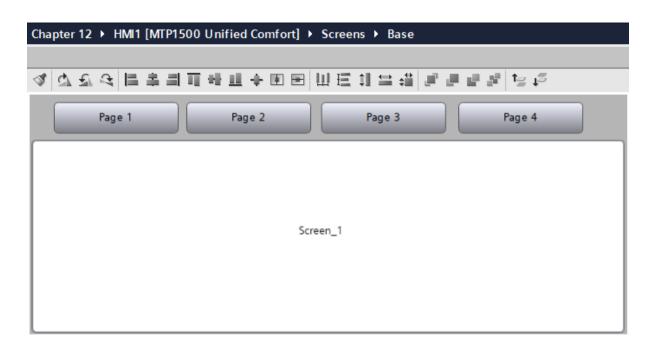


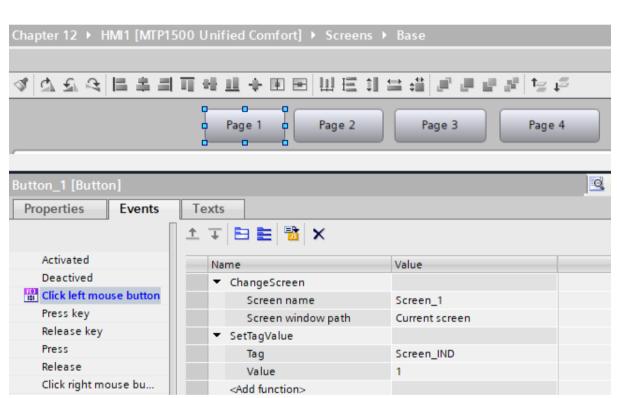


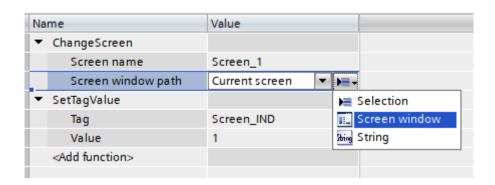


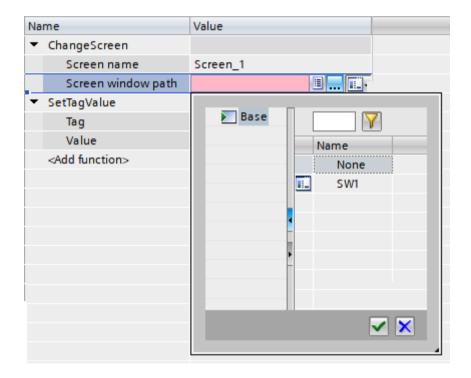
Chapter 12: Managing Navigation and Alarms

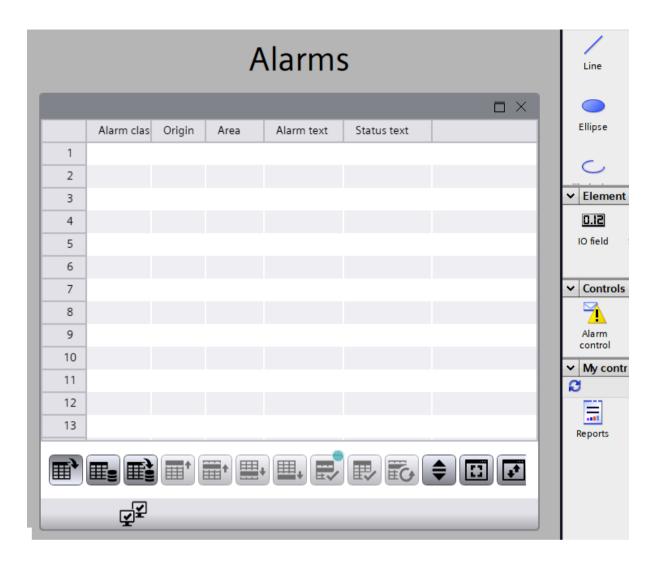


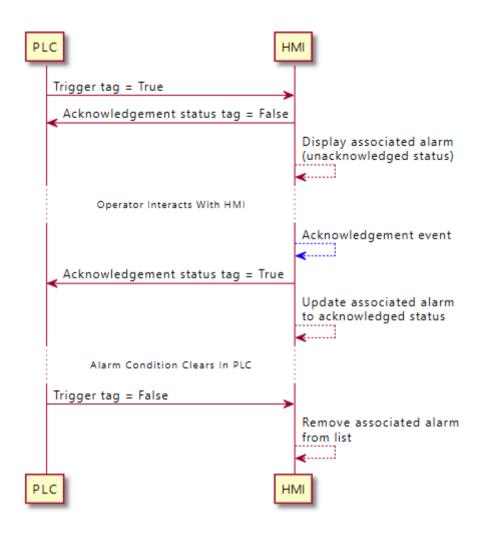


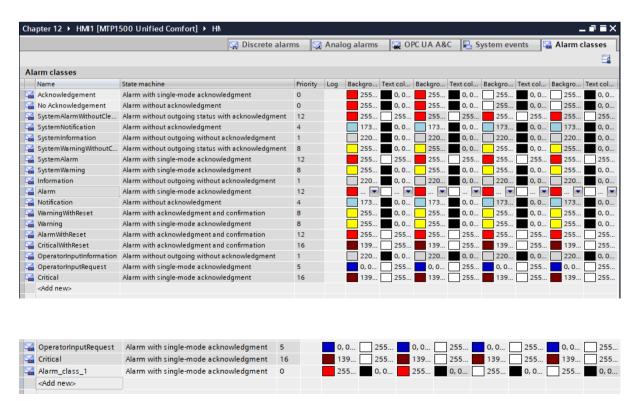


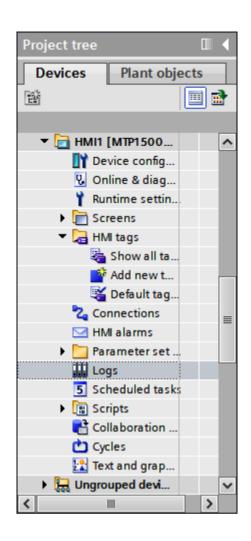


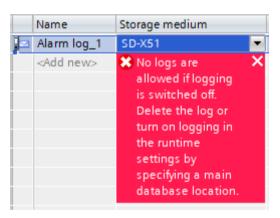




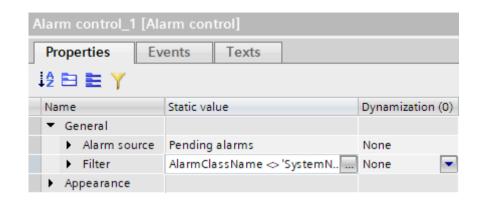


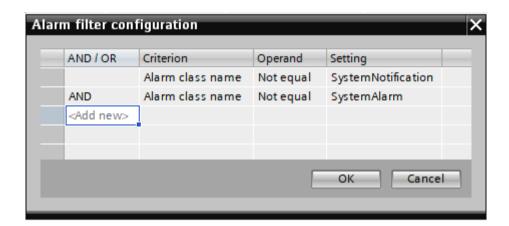




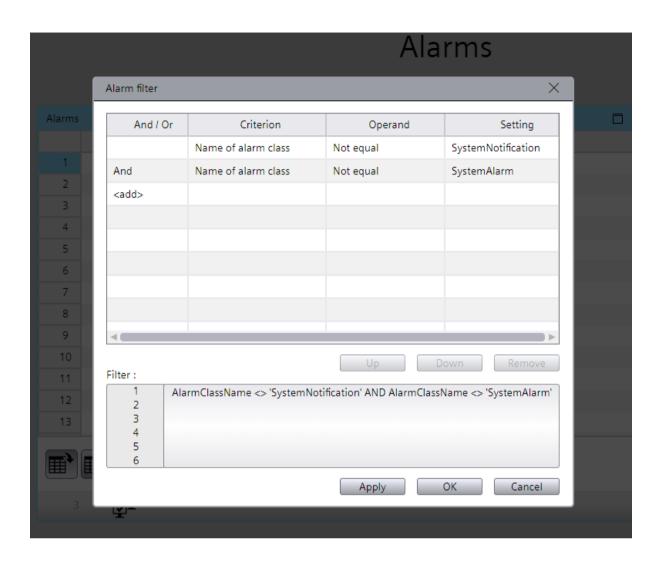


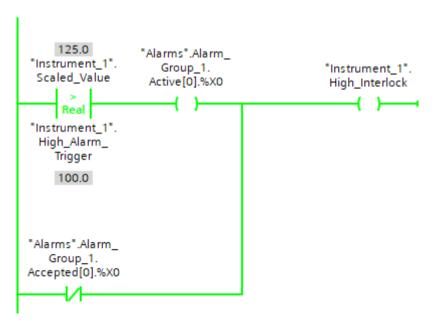


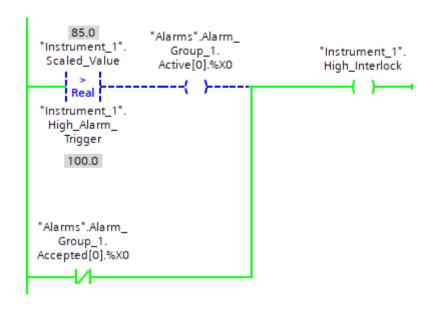


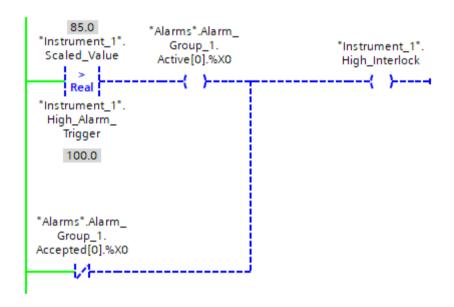


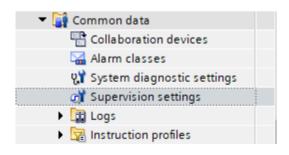


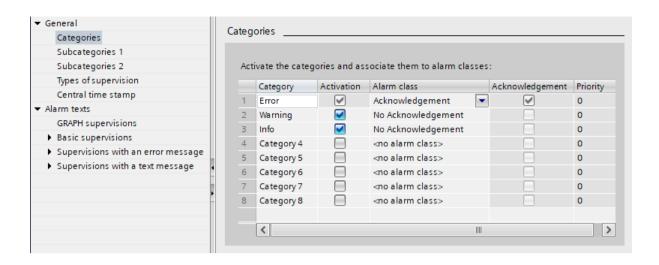


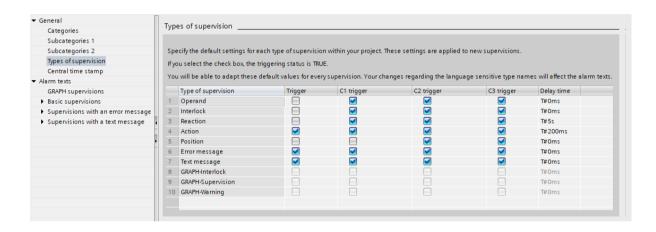


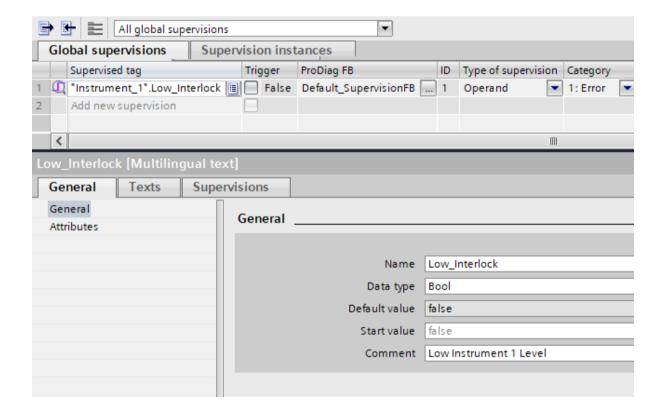


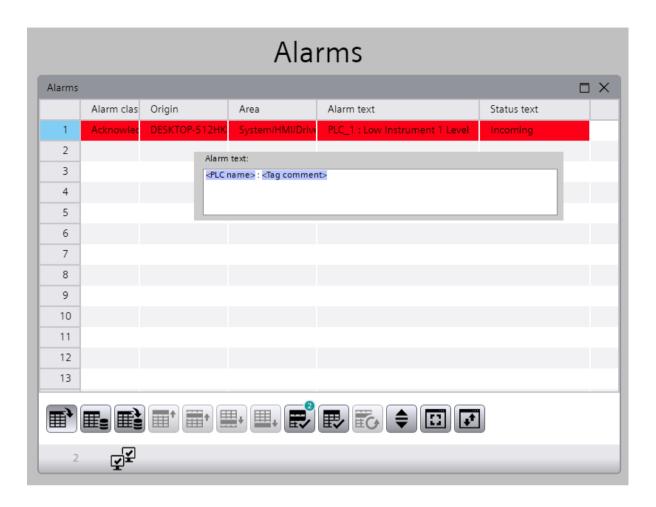


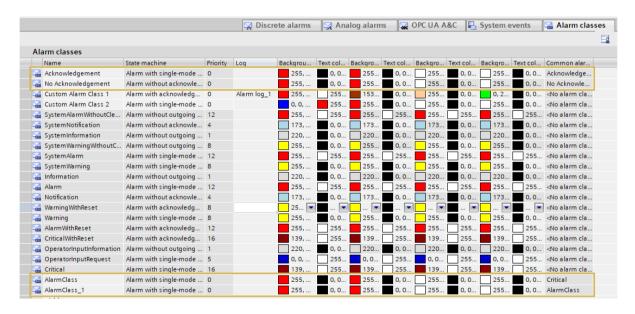




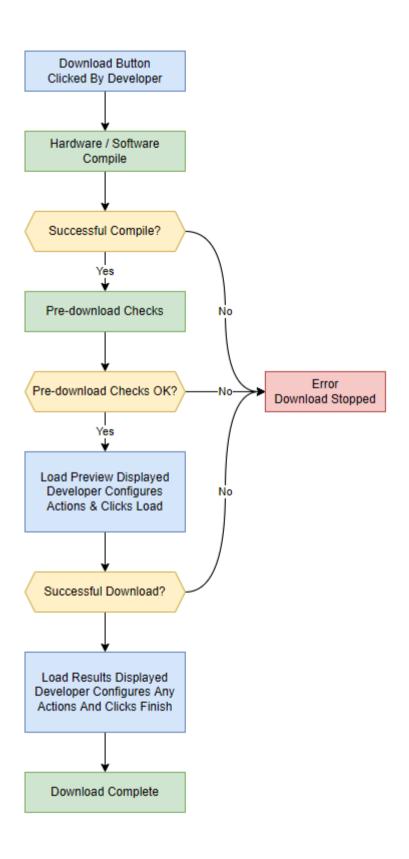




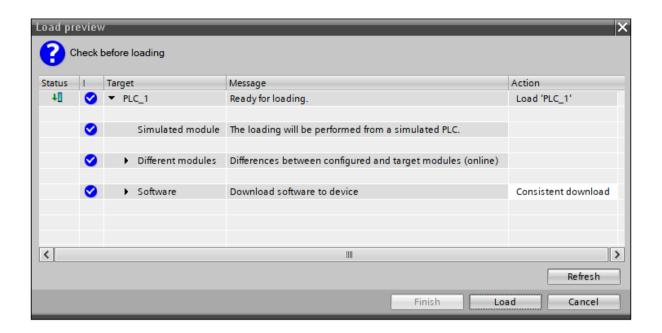


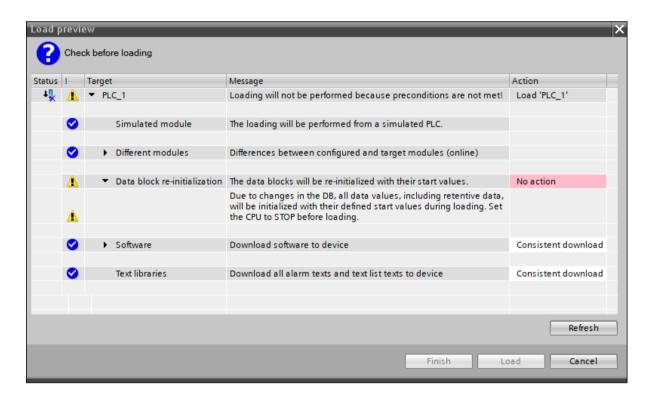


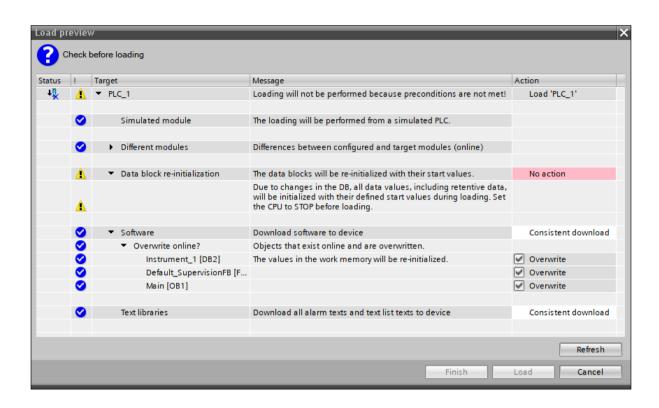
Chapter 13: Downloading to the PLC

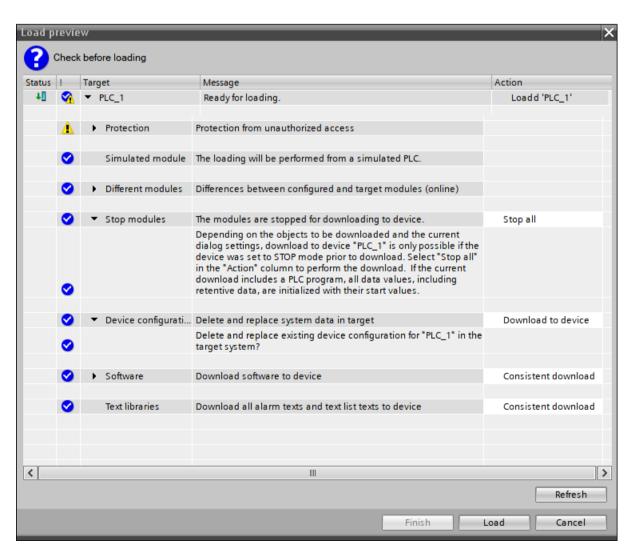












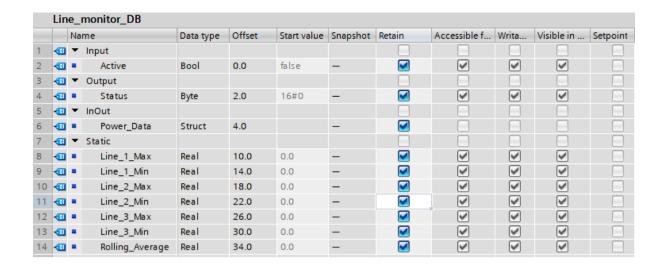
		Na	me	Data type	Start value	Retain
1	1	•	Static			
2	1	•	Scaled_Value	Real	0.0	
3	1	•	High_Alarm_Trigger	Real	45.0	
4	1	•	High_Interlock	Bool	false	
5	1	•	Low_Interlock	Bool	false	
6	1	•	Low_Low_Interlock	Bool	false	
7	1	•	Scaled_Max	Real	0.0	✓
8	1	•	Scaled_Min	Real	0.0	✓
9	1	•	Raw_Max	Int	0	\checkmark
10	1	•	Raw_Min	Int	0	~

		Na	me	Data type	Default value	Retain
1	1	•	Input			
2	1	•	Active	Bool	false	Non-retain
3	1	•	Output			
4	1		Status	Byte	16#0	Non-retain
5		•	<add new=""></add>			
6	1	•	InOut			
7	1	•	▶ Power_Data	Struct		
8		•	<add new=""></add>			
9	1	•	Static			
10	1	•	Line_1_Max	Real	0.0	Set in IDB
11	1		Line_1_Min	Real	0.0	Set in IDB
12	1		Line_2_Max	Real	0.0	Set in IDB
13	1	•	Line_2_Min	Real	0.0	Set in IDB
14	1	•	Line_3_Max	Real	0.0	Set in IDB
15	1	•	Line_3_Min	Real	0.0	Set in IDB
16	1	•	Rolling_Average	Real	0.0	Retain
17	1	•	Temp			
18	1		► Temp_Array	Array[020] of Real		

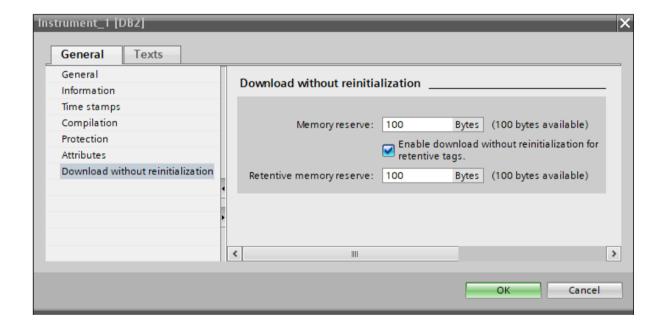
		Na	me	Data type	Start value	Snapshot	Retain
1	€00	•	Input				
2	€00	•	Active	Bool	false	_	
3	€00	•	Output				
4	€11	•	Status	Byte	16#0	_	
5	1	•	InOut				
6	4 11	٠	Power_Data	Struct		_	
7	€11	•	Static				
8	€11	٠	Line_1_Max	Real	0.0	_	
9	€11	•	Line_1_Min	Real	0.0	_	
10	€	•	Line_2_Max	Real	0.0	_	
11	€11	•	Line_2_Min	Real	0.0	_	
12	€00	•	Line_3_Max	Real	0.0	_	
13	€00	•	Line_3_Min	Real	0.0	_	
14	1	•	Rolling_Average	Real	0.0	_	✓

		Na	me	Data type	Start value	Snapshot	Retain
1	40	•	Input				
2	40	•	Active	Bool	false	_	
3	1	•	Output				
4	1	•	Status	Byte	16#0	_	
5	40	•	InOut				
6	40	•	Power_Data	Struct		-	
7	40	•	Static				
8	40	•	Line_1_Max	Real	0.0	-	\checkmark
9	1	•	Line_1_Min	Real	0.0	_	~
10	1	•	Line_2_Max	Real	0.0	_	\checkmark
11	1	•	Line_2_Min	Real	0.0	_	\checkmark
12	1	•	Line_3_Max	Real	0.0	_	~
13	1	•	Line_3_Min	Real	0.0	-	\checkmark
14	1	•	Rolling_Average	Real	0.0	-	✓

		Na	me	Data type	Offset	Default value	Accessible f	Writa	Visible in	Setpoint	Supervision	Comment
1	1	•	Input									
2	1	•	Active	Bool	0.0	false	✓	~	\checkmark			
3	1	•	Output									
4	1	•	Status	Byte	2.0	16#0	~	~	\checkmark			
5	1	•	InOut									
6	1	•	▶ Power_Data	Struct	4.0							
7	1	•	Static									
8	1	•	Line_1_Max	Real	10.0	0.0	$\overline{\mathbf{v}}$	\checkmark	\checkmark			
9	1	•	Line_1_Min	Real	14.0	0.0	$\overline{\mathbf{v}}$	~	\checkmark			
10	1	•	Line_2_Max	Real	18.0	0.0	$\overline{\mathbf{w}}$	\checkmark	\checkmark			
11	1	•	Line_2_Min	Real	22.0	0.0	$\overline{\mathbf{v}}$	~	\checkmark			
12	1	•	Line_3_Max	Real	26.0	0.0	$\overline{\mathbf{w}}$	\checkmark	\checkmark			
13	1	•	Line_3_Min	Real	30.0	0.0	$\overline{\mathbf{v}}$	\checkmark	\checkmark			
14	1	•	Rolling_Average	Real	34.0	0.0	$\overline{\mathbf{v}}$	\checkmark	\checkmark			
15	1	•	Temp									
16	1	•	► Temp_Array	Array[020] of Real	0.0							
17	1	•	Constant									
18		•	<add new=""></add>									

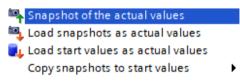


Datablock Memory Occupied By Declared Variables Memory Reserve

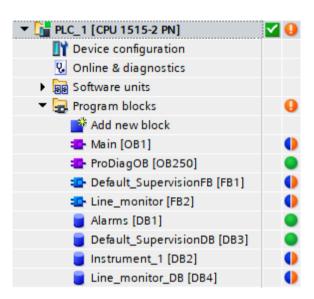


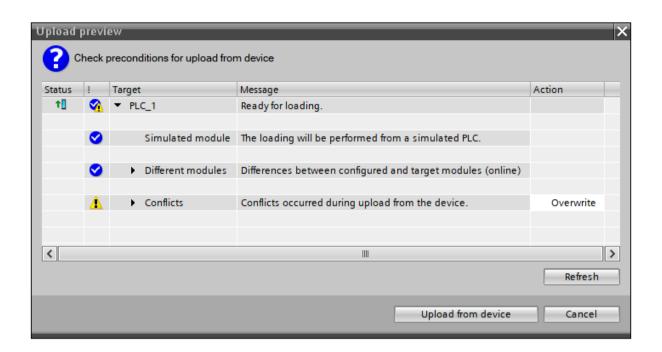


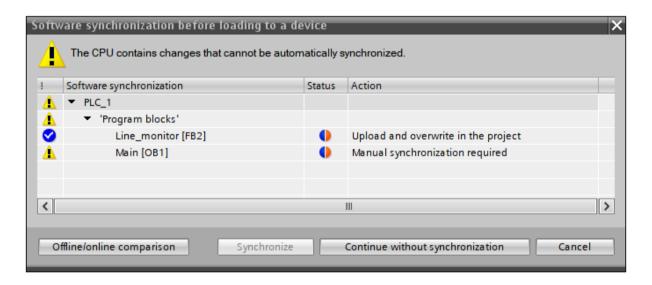
Ala	Alarms (snapshot created: 1/11/2022 11:45:10 PM)										
	Na	me			Data type	Start value	Snapshot				
1	▼ Static										
1	•	•	Ala	rm_Group_1	"UDT_Alarm_Group"						
1		•	•	Accepted	Array[01] of DWord						
1			•	Accepted[0]	DWord	16#0	16#0000_0000				
1			•	Accepted[1]	DWord	16#0	16#0000_0000				
1		•	٠	Active	Array[01] of D						

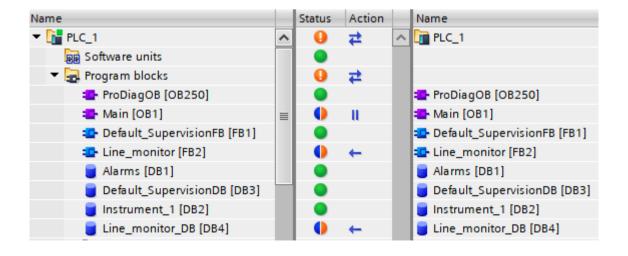




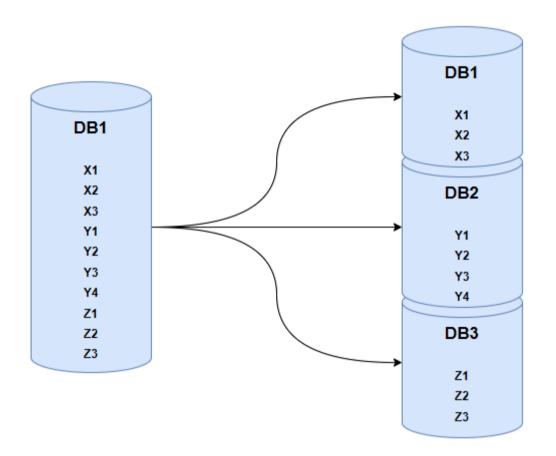






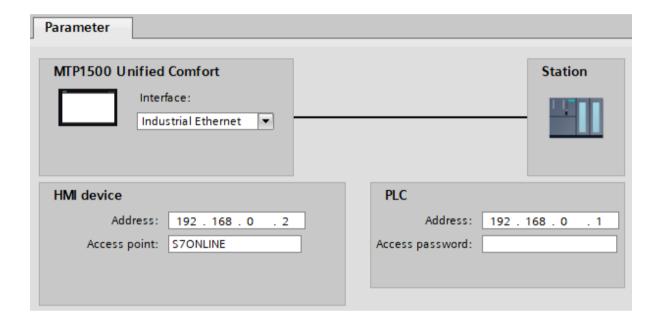




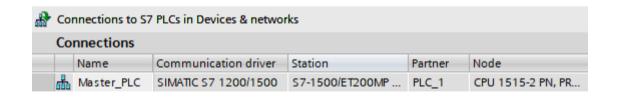


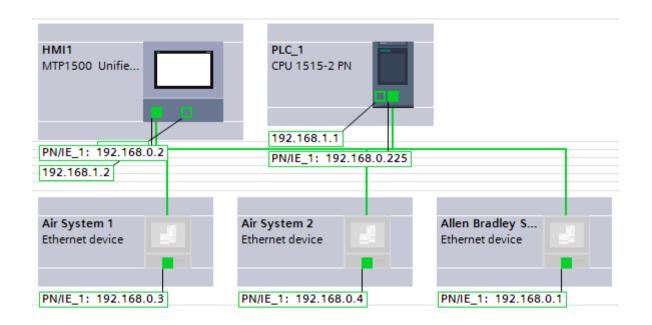
Chapter 14: Downloading to the HMI

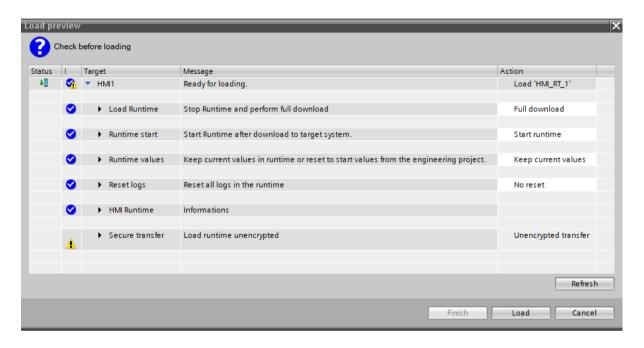


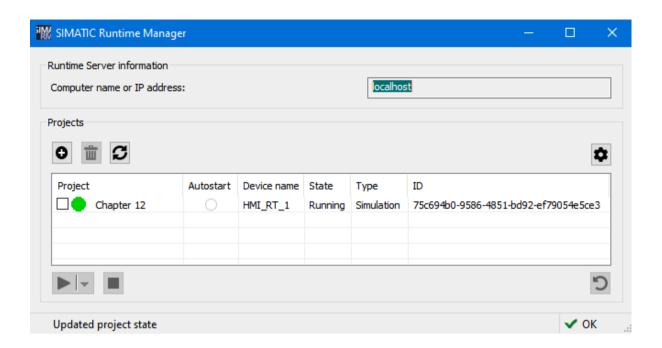


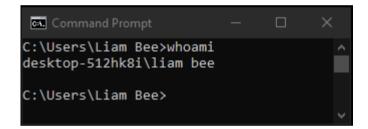










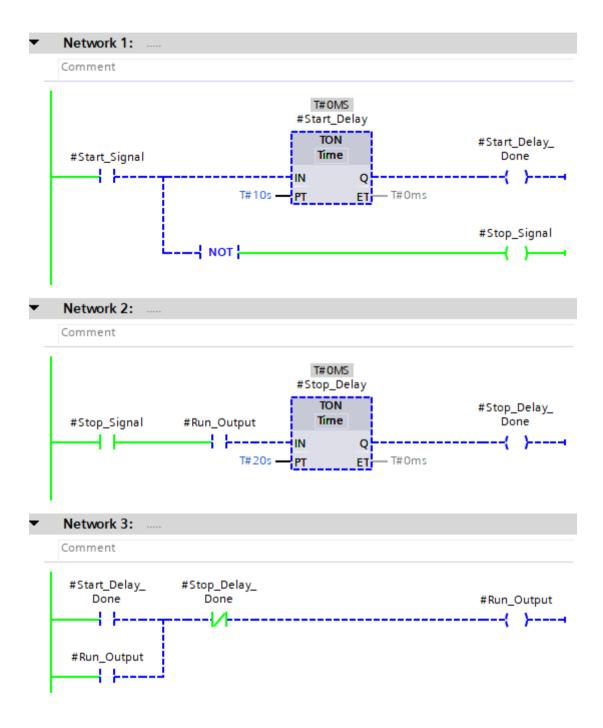


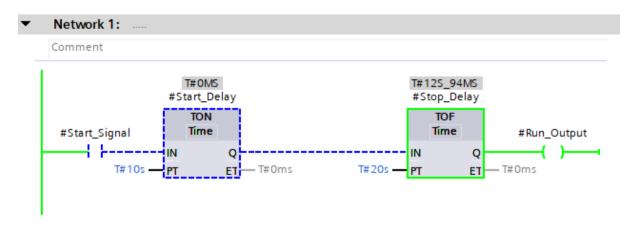


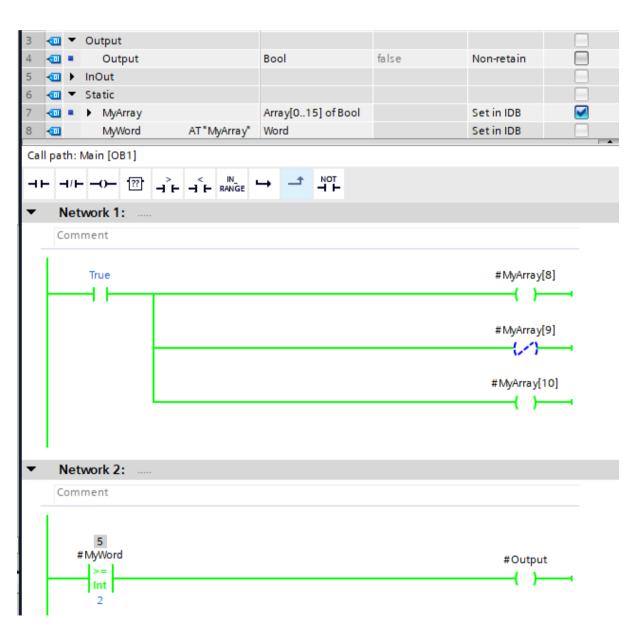




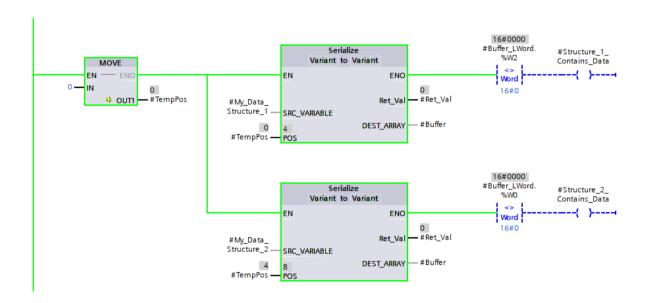
Chapter 15: Programming Tips and Additional Support



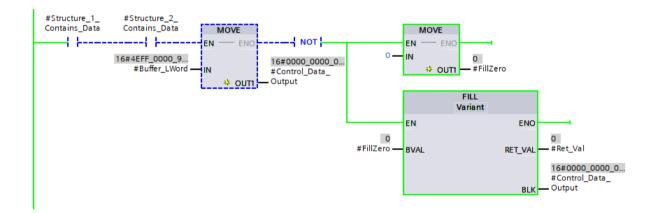




```
//Example 1
□IF #High_level = True AND #System_Running = True THEN
     #High Alarm := True;
 ELSE
     #High Alarm := False;
END_IF;
 //Example 2
□IF #Scale_Value < 20 THEN
     #Signal Healthy := True;
 ELSE
     #Signal_Healthy := False;
END_IF;
    //Example 1
    #High_Alarm := #High_level AND #System_Running;
    //Example 2
    #Signal_Healthy := #Scale_Value < 20;
```

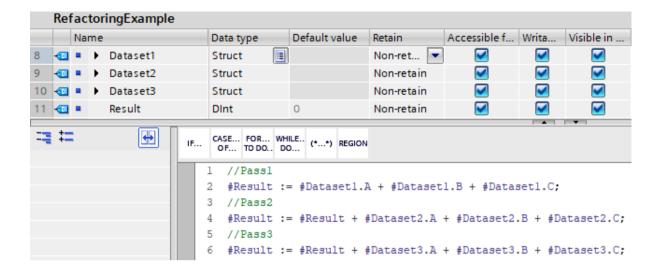


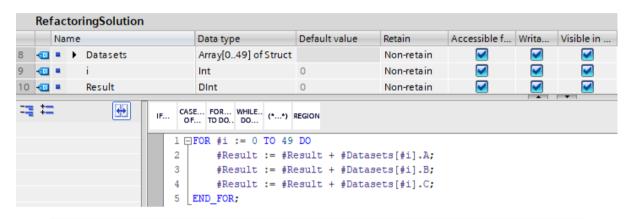
	Na	me			Data type	Offset
€	•	Sta	atic			
1	•	•	My_Data_Structure_1		Struct	8.0
1		٠	Status_Word		Word	8.0
1		•	Control_Word		Word	10.0
1	•	•	My_Data_Structure_2	!	Struct	12.0
1		•	Status_Word		Word	12.0
1		•	Control_Word		Word	14.0
4 □	•	•	Buffer		Array[07] of Byte	16.0
1		•	Buffer[0]		Byte	16.0
1		•	Buffer[1]		Byte	17.0
1		•	Buffer[2]		Byte	18.0
1		•	Buffer[3]		Byte	19.0
1		•	Buffer[4]		Byte	20.0
1		•	Buffer[5]		Byte	21.0
1		•	Buffer[6]		Byte	22.0
1		•	Buffer[7]		Byte	23.0
1			Buffer_LWord	AT "Buffer"	LWord	16.0
€	•		Structure_1_Contains	s_Data	Bool	24.0
€	•		Structure_2_Contains	_Data	Bool	24.1



	Na	me		Data type	Offset	Monitor value
1		In	out			
1	•	Οι	utput			
1	•		Control_Data_Output	LWord	0.0	16#0000_0000_0000_0000
1		In(Out			
1	•	Sta	atic			
1	•	•	My_Data_Structure_1	Struct	8.0	
1		•	Status_Word	Word	8.0	16#4EFF
1			Control_Word	Word	10.0	16#0000
1	•	•	My_Data_Structure_2	Struct	12.0	
1			Status_Word	Word	12.0	16#9024
1		•	Control_Word	Word	14.0	16#0000
1	•	•	Buffer	Array[07] of Byte	16.0	
1			Buffer[0]	Byte	16.0	16#4E
1		•	Buffer[1]	Byte	17.0	16#FF
1		•	Buffer[2]	Byte	18.0	16#00
1		•	Buffer[3]	Byte	19.0	16#00
1		•	Buffer[4]	Byte	20.0	16#90
1		•	Buffer[5]	Byte	21.0	16#24
1		•	Buffer[6]	Byte	22.0	16#00
1		•	Buffer[7]	Byte	23.0	16#00
1	•		Structure_1_Contains_Data	Bool	24.0	FALSE
1	•		Structure_2_Contains_Data	Bool	24.1	FALSE

	Nar	me		Data type	Offset	Monitor value
40		In	put			
41	•	Οι	utput			
1	•		Control_Data_Output	LWord	0.0	16#4EFF_1234_9024_5678
1		In	Out			
1	•	St	atic			
41	•	•	My_Data_Structure_1	Struct	8.0	
41		•	Status_Word	Word	8.0	16#4EFF
1		•	Control_Word	Word	10.0	16#1234
€	•	•	My_Data_Structure_2	Struct	12.0	
1		•	Status_Word	Word	12.0	16#9024
1		•	Control_Word	Word	14.0	16#5678
1	•	•	Buffer	Array[07] of Byte	16.0	
40		•	Buffer[0]	Byte	16.0	16#4E
€		•	Buffer[1]	Byte	17.0	16#FF
€		•	Buffer[2]	Byte	18.0	16#12
€		•	Buffer[3]	Byte	19.0	16#34
€		•	Buffer[4]	Byte	20.0	16#90
€		•	Buffer[5]	Byte	21.0	16#24
41		•	Buffer[6]	Byte	22.0	16#56
40			Buffer[7]	Byte	23.0	16#78
40	•		Structure_1_Contains_Data	Bool	24.0	TRUE
€	•		Structure_2_Contains_Data	Bool	24.1	TRUE





Network 1: Wait For System Ready

Comment

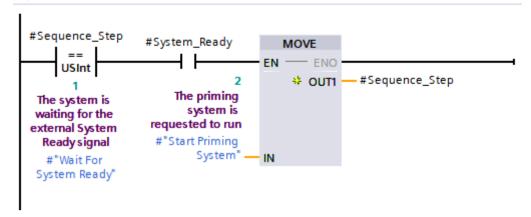
```
#Sequence_Step #System_Ready MOVE

== USInt ENO ENO #Sequence_Step

1 2 IN ** OUT1 #Sequence_Step
```

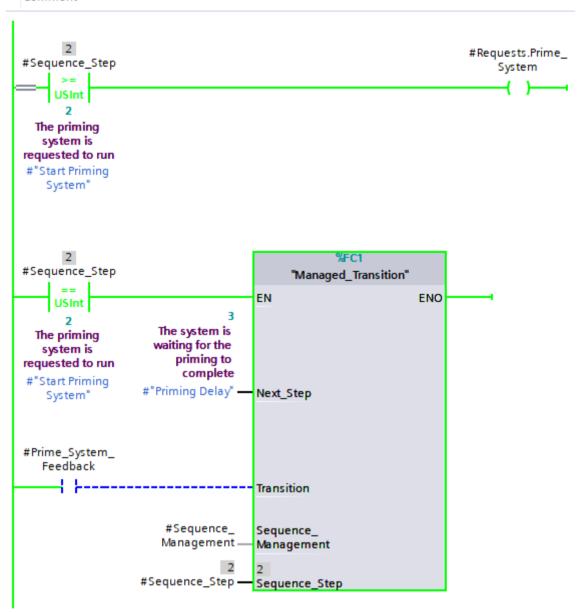
Network 2: Wait For System Ready

Comment

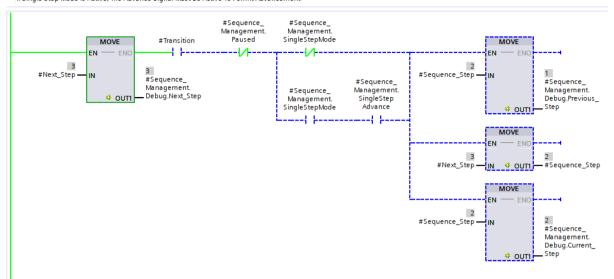


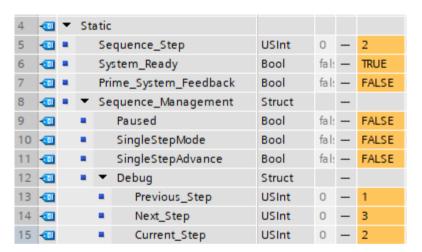
Network 3: Wait For System Ready

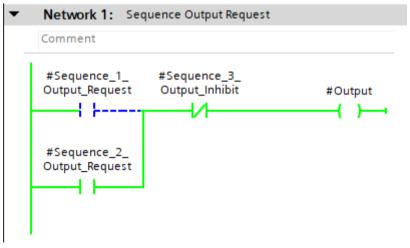
Comment

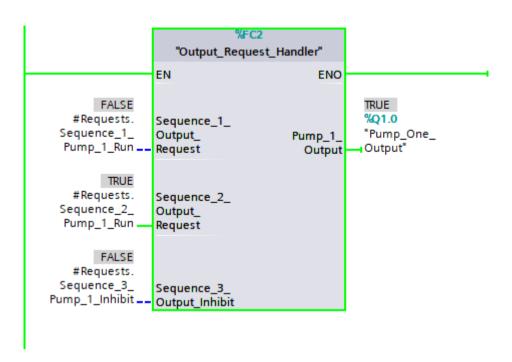


Only Allow Transition If The System Is NOT Paused
 If Single Step Mode Is Active, The Advance Signal Must Be Active To Permit Advancement









Ma	Main_Pump_Floor_1									
	Na	me					Data type	Comment		
1	•	Sta	atic							
411		٠	Ch	nem	ica	_Dosing_Area_1	Struct	Location C1-DFA233		
411	■ ▼ Chemical_Dosing_Area_2				ica	_Dosing_Area_2	Struct	Location C2-UVTA233		
411	■ ▶ System_1				stei	m_1	Struct	System 1 - Acid Dosing		
1		■ ▼ System_2				m_2	Struct	System 2 - Caustic Dosing		
1			•	•	Pu	mpSet_A	Struct	Caustic Delivery Pumpset		
1				•	٠	Pump_1	Struct	Asset: 123A		
411				•	٠	Pump_2	Struct	Asset: 456A		
411				•	•	Pump_3	Struct	Asset: 789A		
411					•	Running	Bool			
1			•	•	Pu	mpSet_B	Struct	Emergency PumpSet		

```
0 = Disabled, 1 =
Enabled Forward,
  2 = Enabled
Reverse, 3 = Jog
Forward, 4 = Jog
    Reverse
     #Mode
                                            #Enabled
      I==
                                              -( )-----
      Int
0 = Disabled, 1 =
Enabled Forward,
  2 = Enabled
Reverse, 3 = Jog
Forward, 4 = Jog
    Reverse
     #Mode
      == 
      Int
```

```
1 //Call Timers
 3 //Timer 1
 4 □ #Timerl(IN:=#Timer_l_In,
           PT:=T#10s,
            Q=>#Timer 1 DN);
 6
8 //Timer 2
10 p#Timer2(IN := #Timer 2 In,
           PT := T#5s,
11
            Q \Rightarrow #Timer 2 DN);
12
13
14 //Start Timer 1
15 #Timer_1_In := NOT #Timer_2_DN; //When Timer 2 is not done, run Timer 1
16
17 //Start Timer 2
18 #Timer_2_In := #Timer_1_DN; //When Timer 1 is done, run Timer 2
19
20 //Set Outputs
21 #Dwell Cycle Active := NOT #Timer 1 DN;
22 #Run Cycle Active := #Timer 1 DN;
23 #Cycle Complete Pulse := #Timer 2 DN;
```

```
1 // =
2 // | Dwell/Run Timer Block - Vl.2 - LBEE - 10/02/22
3 // This block runs a dwell cycle (10s), immediately followed by a run cycle (5s)
4 // A cycle complete output is pulsed on a full Dwell and Run cycle
5 // Both the Dwell and the Run cycles also have an output to represent when in those states
6 // 🖳
8 // //==============\\
                  Call Timers
10 // |]------
11 // || Calls to the TON Function blocks for the timers
                                                          -11
12 // || Note - The IN and Q variables are used elsewhere in the code, cross reference if unsure ||
13 // \\=========
15
      // +-----
16
      // | Timer 1 | Dwell Timer
17
       // +-----+
       // | | Called to run when Timer 2 (Run Timer) is not done (Completed) |
18
19
       // +-----
20 ⊟
       #Timerl(IN:=#Timer 1 In,
21
           PT:=T#10s,
22
            Q=>#Timer_1_DN);
23
24
       #Timer_1_In := NOT #Timer_2_DN;
25
       // +----
27
       // | Timer 2 |
                       Run Timer
28
       // +-----
29
       // | | Called to run when Timer 1 (Dwell Timer) is done (Completed) |
30
       // +-----
31 ⊟
       #Timer2(IN := #Timer_2_In,
           PT := T#5s,
32
33
            Q => #Timer_2_DN);
34
35
       #Timer_2_In := #Timer_1_DN;
36
37 // //=============\\
                       Set Outputs
38 // 11
                                                        - 11
39 // |]------[|
40 \ //\ || Pass the status of the Timers and if the Cycle has completed to the interface outputs ||
41 // \\------//
42
43 #Dwell_Cycle_Active := NOT #Timer_1_DN;
44 #Run Cycle Active := #Timer 1 DN;
45 #Cycle_Complete_Pulse := #Timer_2_DN;
```

```
A | Y
== ==
             4
                    IF... CASE... FOR... WHILE.. (*...*) REGION
▼ P Call Timers
▶ P Set Outputs
                      2 // Dwell/Run Timer Block - V1.2 - LBEE - 10/02/22
                      3 // This block runs a dwell cycle (10s), immediately followed by a run cycle (5s)
                      4 // A cycle complete output is pulsed on a full Dwell and Run cycle
5 // Both the Dwell and the Run cycles also have an output to represent when in those states
                      6 // L
                      8 =REGION Call Timers
                            // //===
                      10
                            // 11
                                                                   Call Timers
                                                                                                                1.1
                            11
                                                                                                                = [ ]
                           // || Calls to the TON Function blocks for the timers || // || Note - The IN and Q variables are used elsewhere in the code, cross reference if unsure ||
                      12
                      13
                            // \\======
                      14
                      15
                      16
                            // +----
                            // | Timer 1 | Dwell Timer
                      17
                      18
                            // +======+===
                            // | | Called to run when Timer 2 (Run Timer) is not done (Completed) |
                      19
                             // +----
                      20
                            #Timerl(IN := #Timer_l_In,
                      21 占
                             PT := T#10s,
Q => #Timer_1_DN);
                     22
                      23
                     24
                           #Timer_1_In := NOT #Timer_2_DN;
                      25
                      26
                            // +----
                      27
                            // | Timer 2 |
                      28
                                                              Run Timer
                            // +=====+===+====
                      29
                            // | | Called to run when Timer 1 (Dwell Timer) is done (Completed) |
                      30
                            // +----
                      31
                          #Timer2(IN := #Timer_2_In,
                     32 🖨
                             PT := T#5s,
                      33
                                  Q => #Timer_2_DN);
                      34
                      35
                            #Timer_2_In := #Timer_1_DN;
                      36
                     37 END_REGION
                      38
                     39 ⊞REGION Set Outputs
```

Information System 🗶

All information at a glance

In the TIA Portal information system, you will find all the background information, step-by-step instructions and examples that you need for working with the TIA Portal.

Click on one of the following topics for a brief introduction:



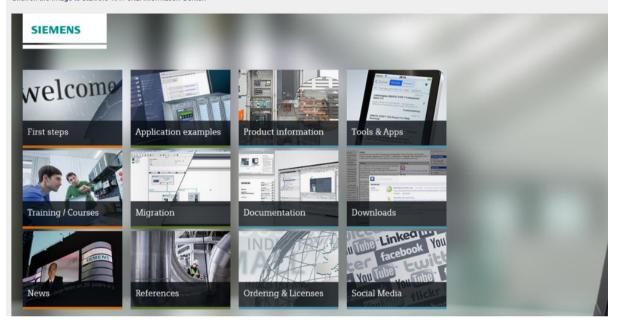




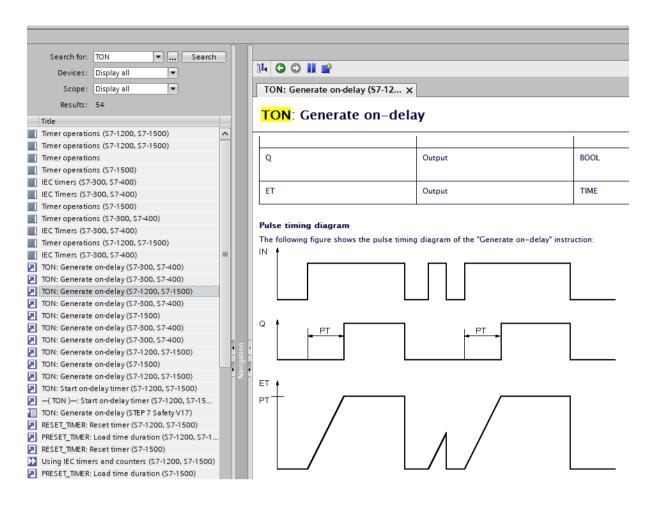


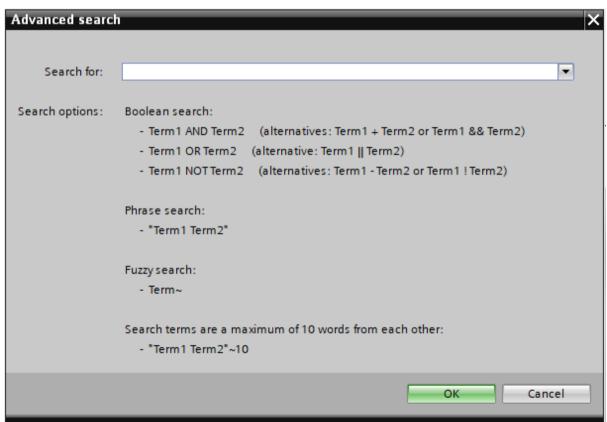
We provide further interesting information relating to the TIA Portal for you in the TIA Portal Information Center. You require an Internet connection to access this Information Center.

Click on the image to start the TIA Portal Information Center:



- Collapsed Category Folder
- Open Category Folder
- ☐ Factual Information
- Operating Instructions
- Example
- Reference







Liam BeePLC Automation Professional

